

	1	2	3	4	5	6	7	8																																																																														
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	HSF Property : Halogen-Free																																																																																					
B	<div><h1>SS11 AMD Muxless</h1><h2>PIXIES 15"</h2><p>www.altech1.ru</p><h2>Llano FS1/Hudson M3</h2><h3>DB1 (X02)</h3><div>2010.10.15</div></div>																																																																																					
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DATE	CHANGE NO.	REV																																																																																				
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SHEET 1		OF 60																																																																																				
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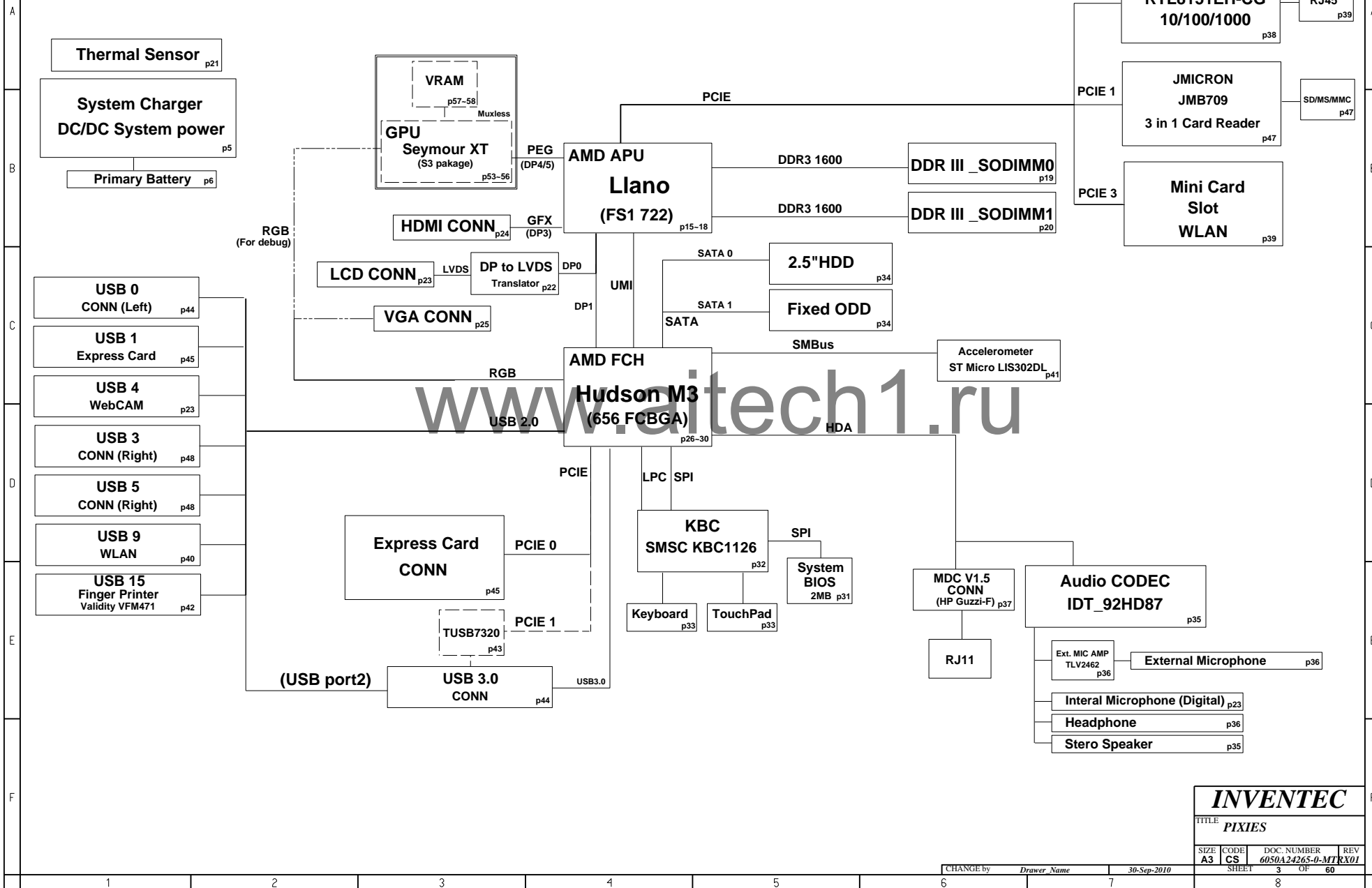
INVENTEC

TITLE			
Contents			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTR	X01
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CHANGE by *Drawer Name* 30-Sep-2010

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Hudson M2 (B SERIES : 6019B0775601)
218-0755002



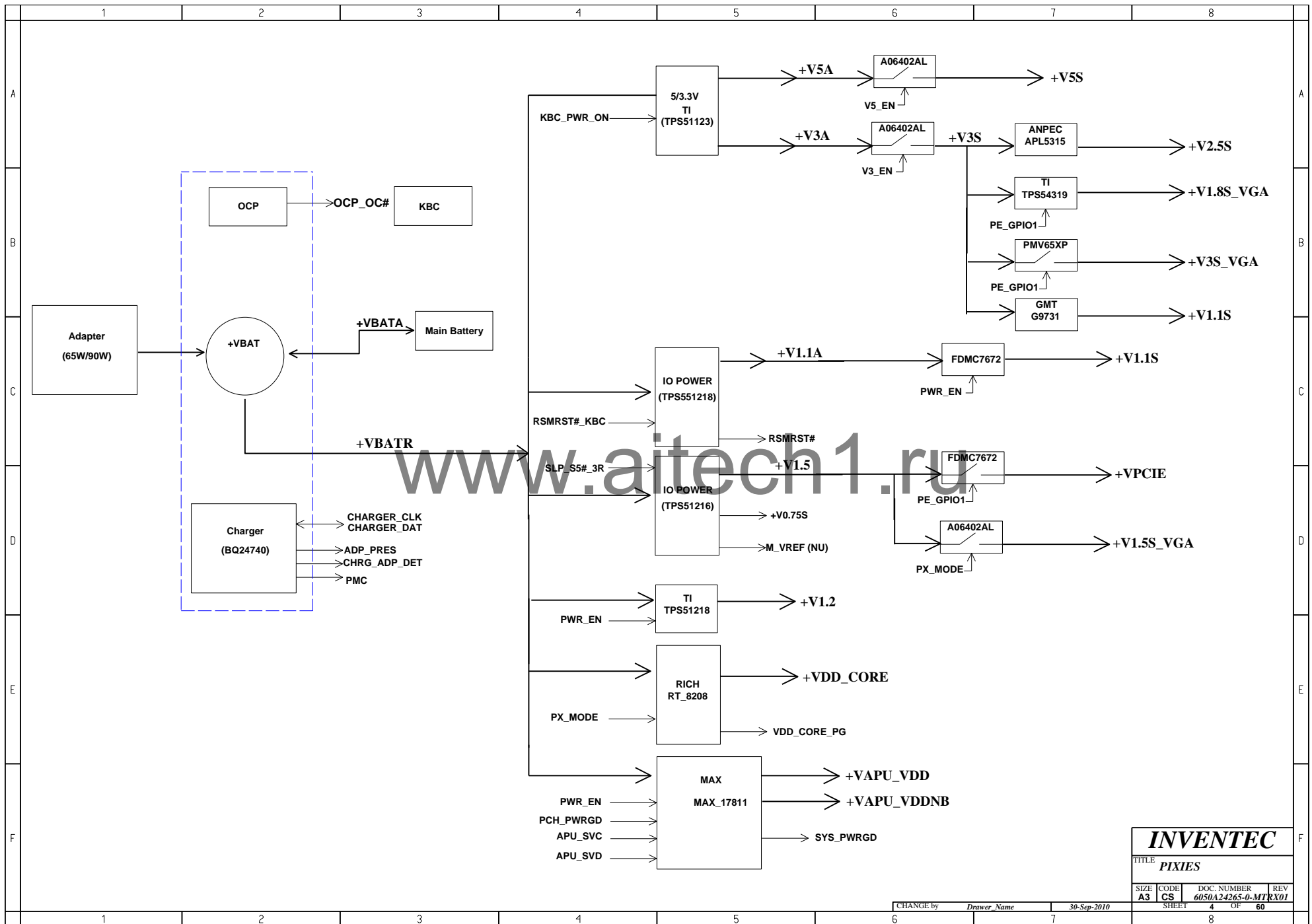
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TITLE
PIXIES

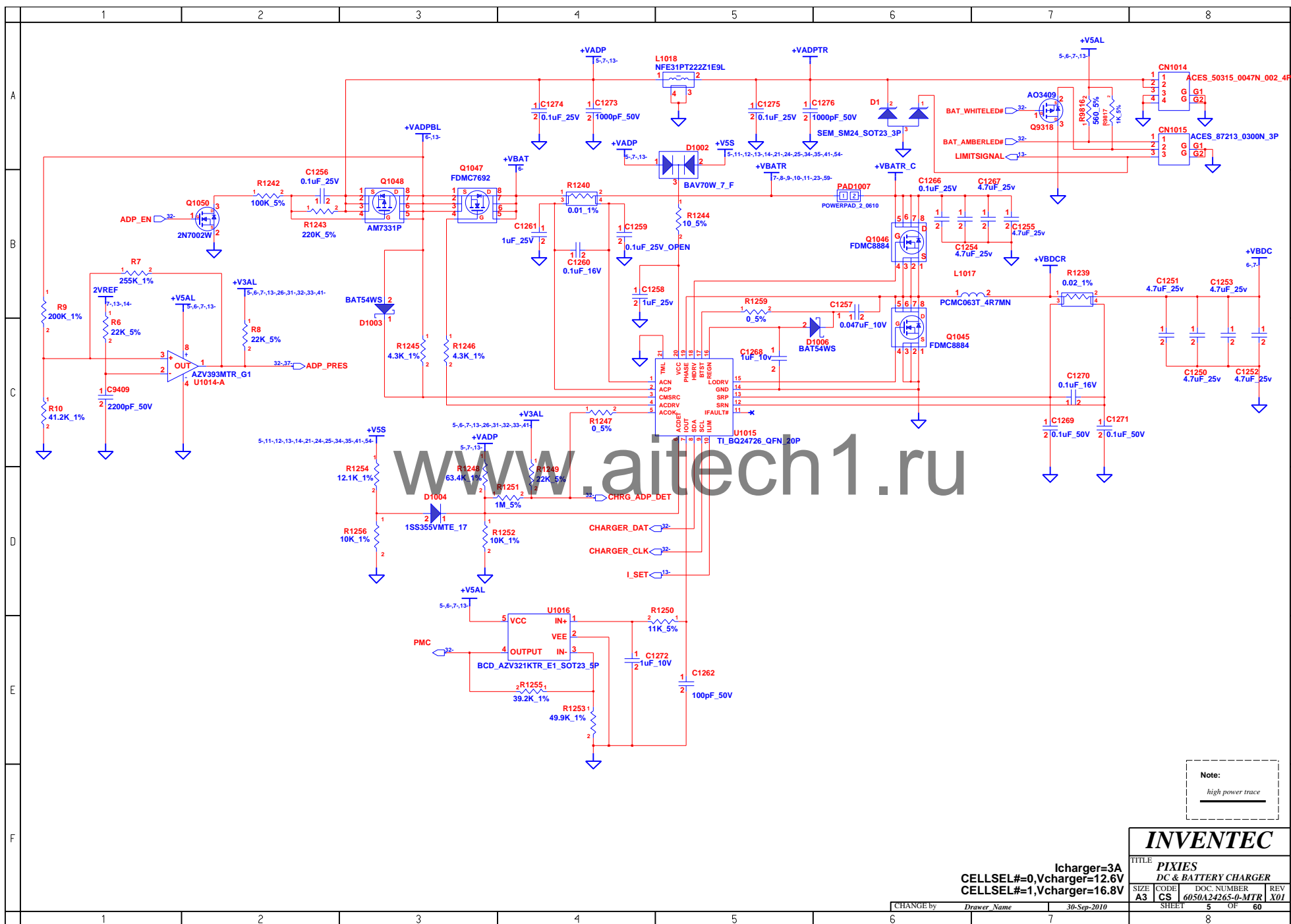
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A3	CS	6050A24265-0-MTR	01

CHANGE by *Drawer Name* 30-Sep-2010

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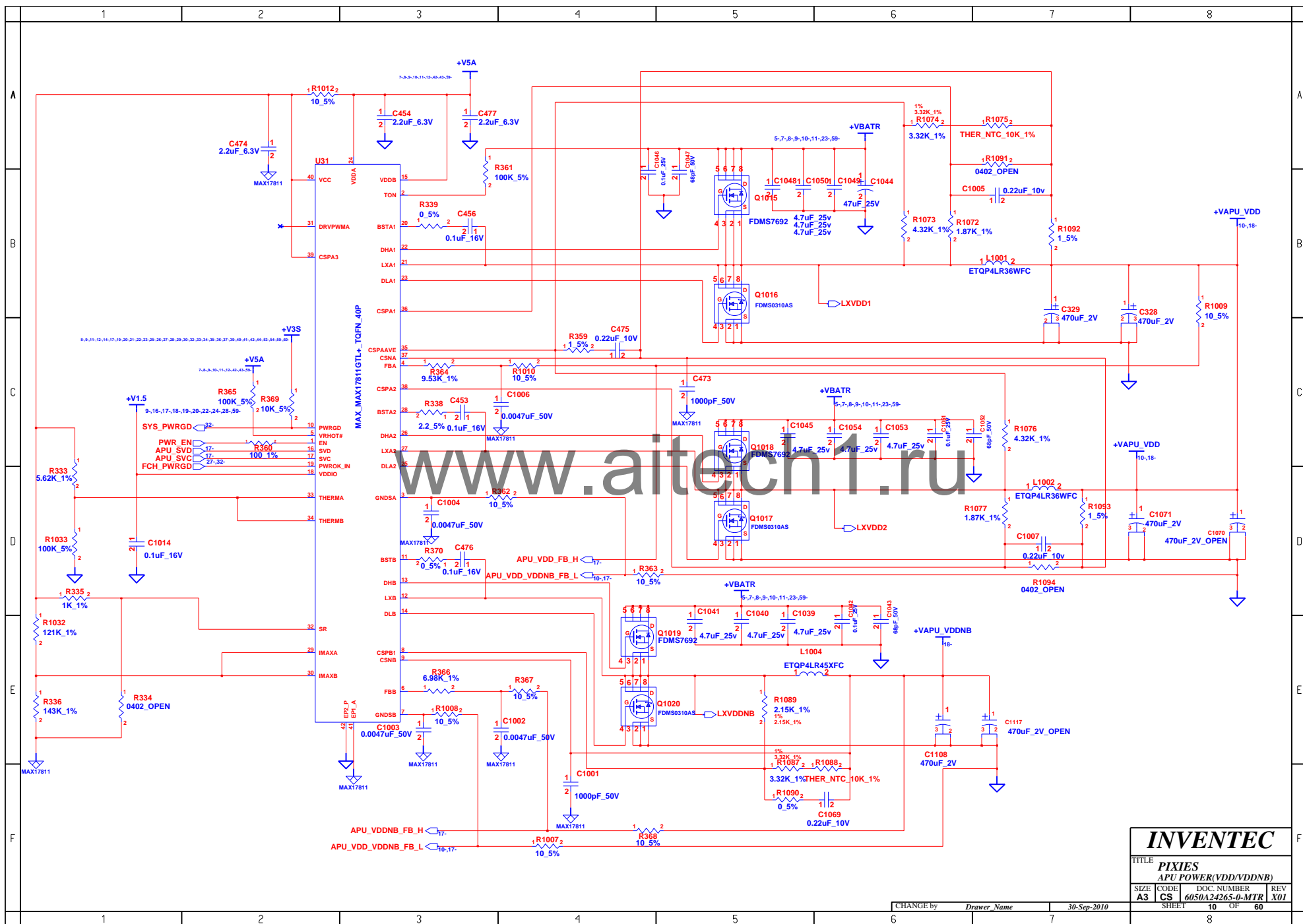
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TITLE PIXIES			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 1
CHANGE by Drawer Name		30-Sep-2010	
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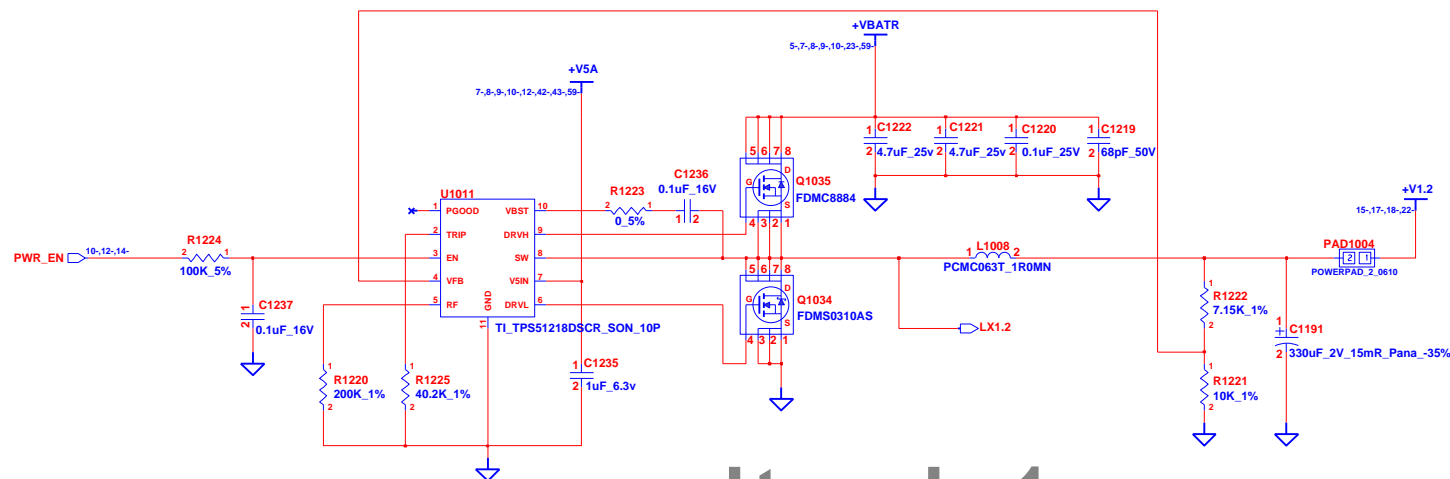




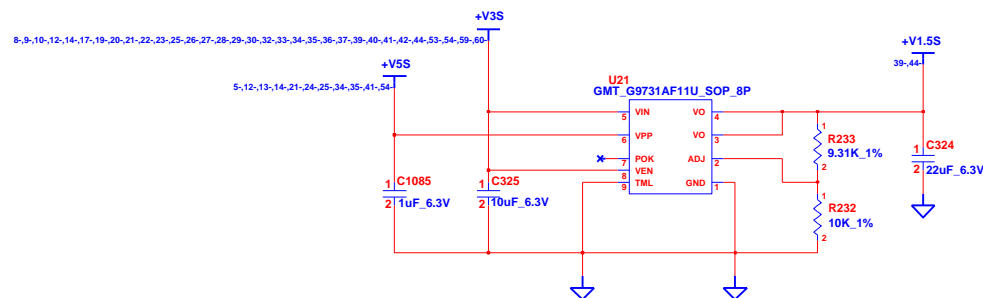
CHANGE by	<i>Drawer_Name</i>	<i>30-Sep-2010</i>
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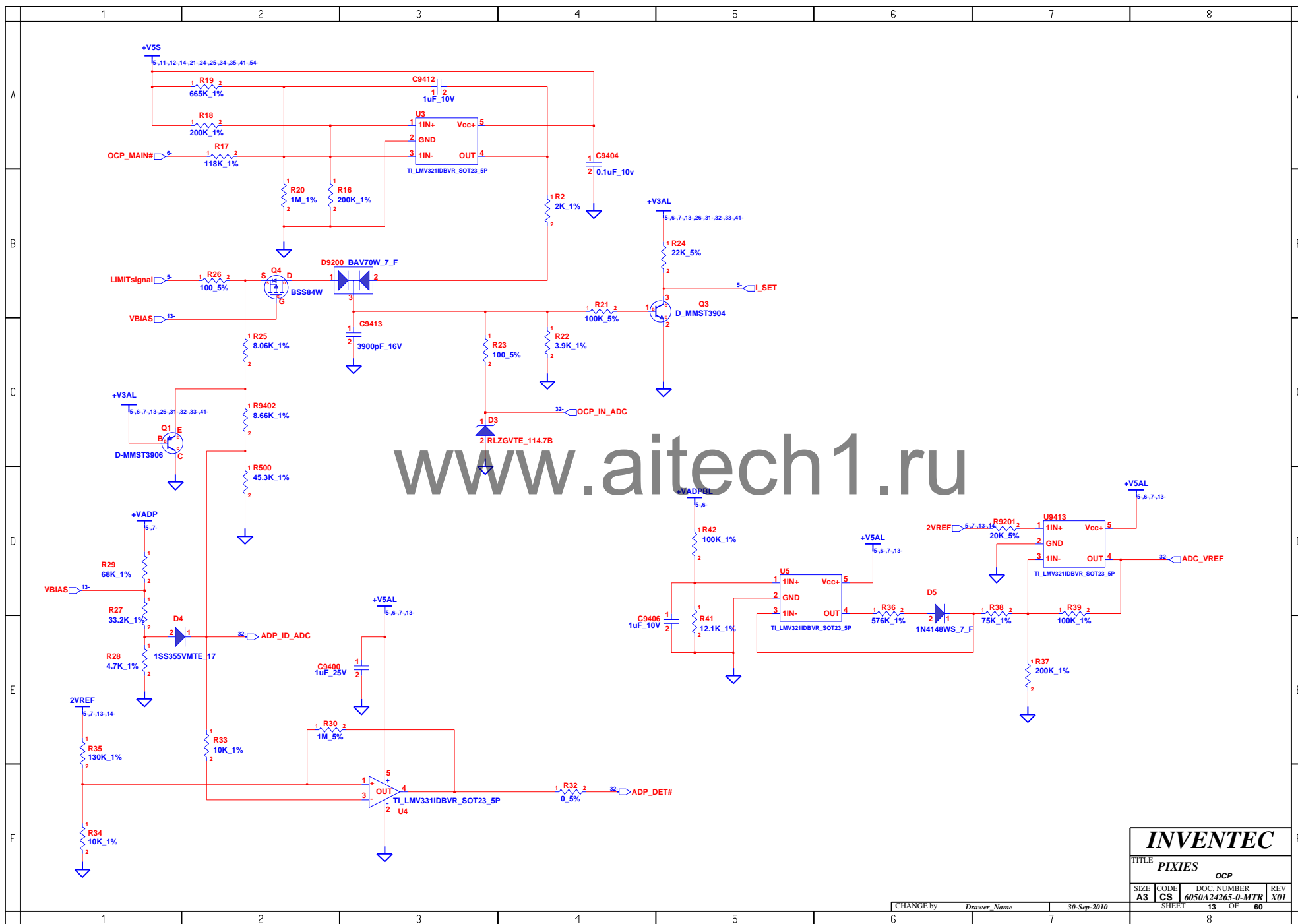
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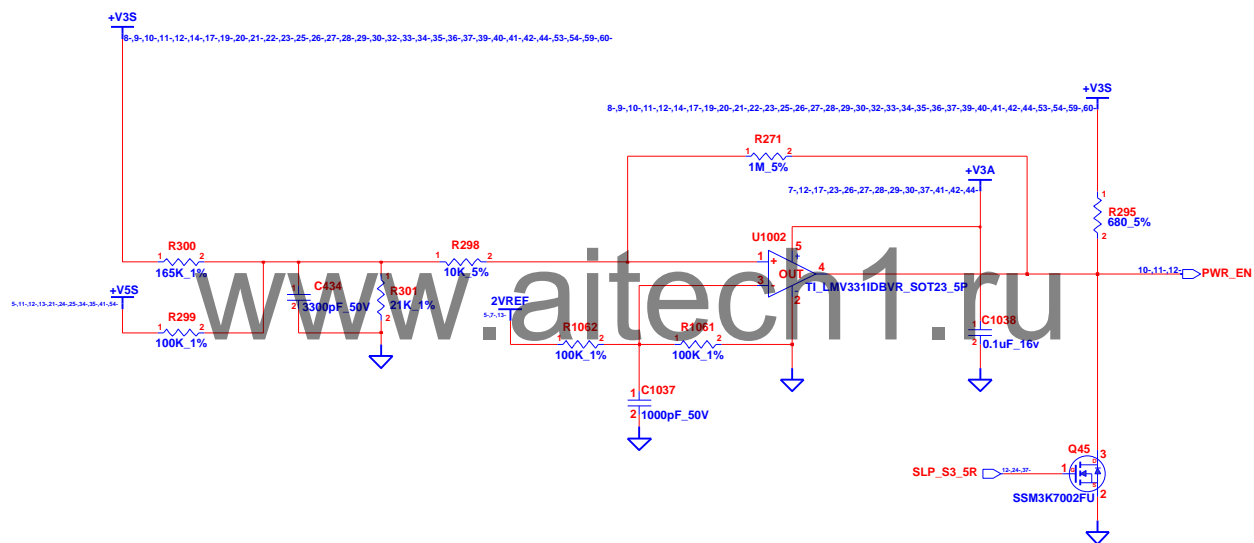
TITLE
PIXIES
SYSTEM POWER VDDP,VDDR(+V1.2)

SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
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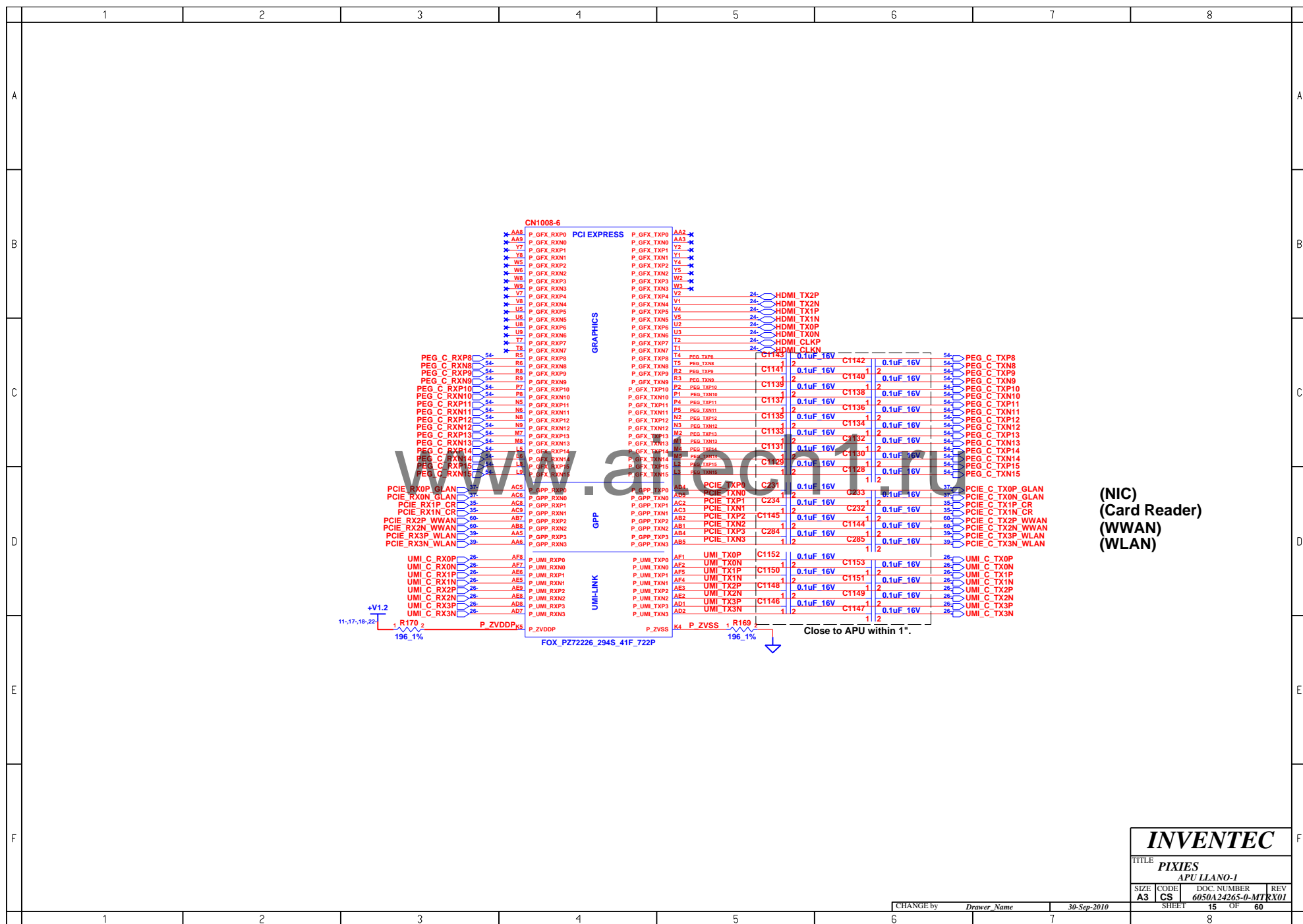
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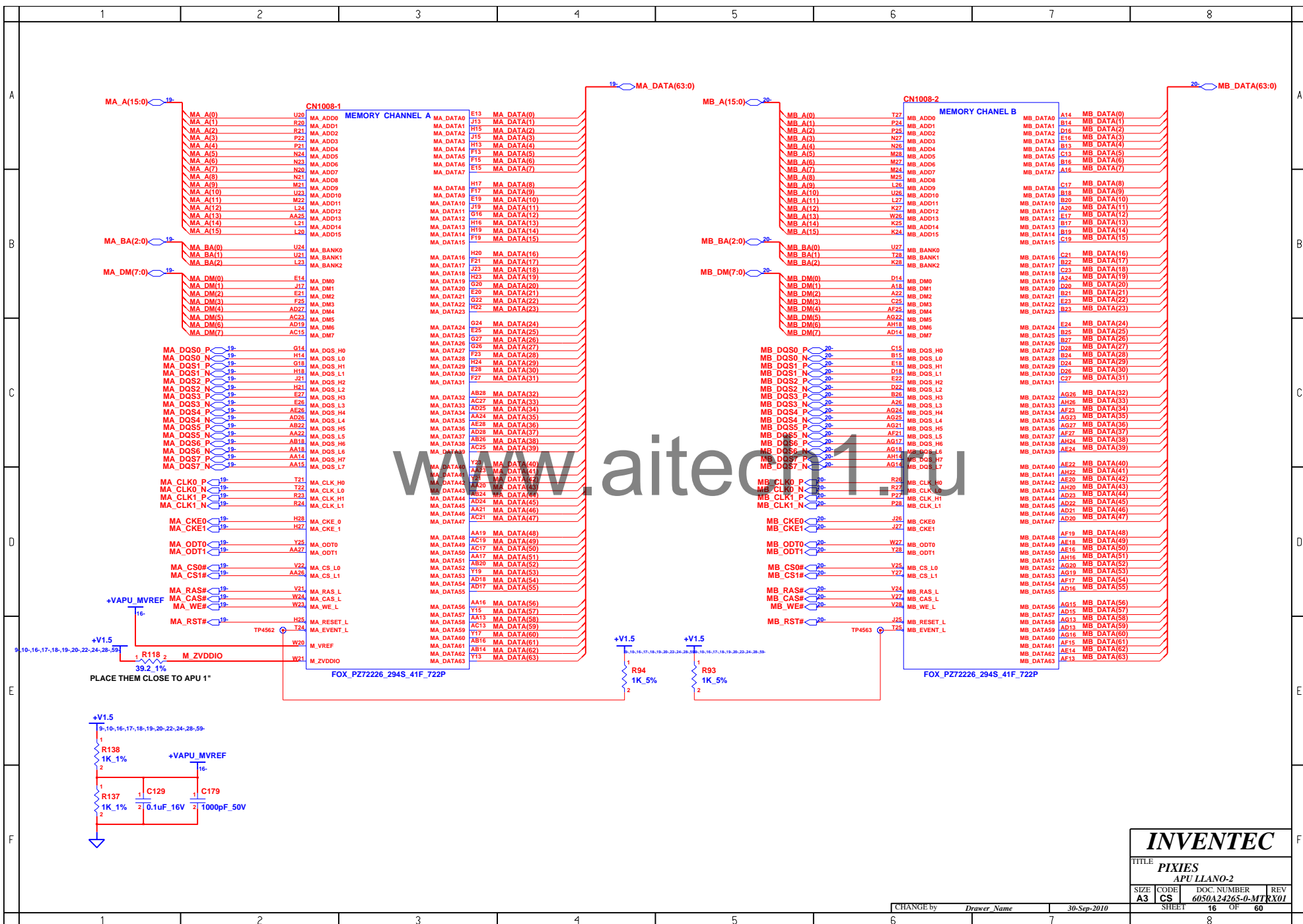
SHEET 11 OF 60

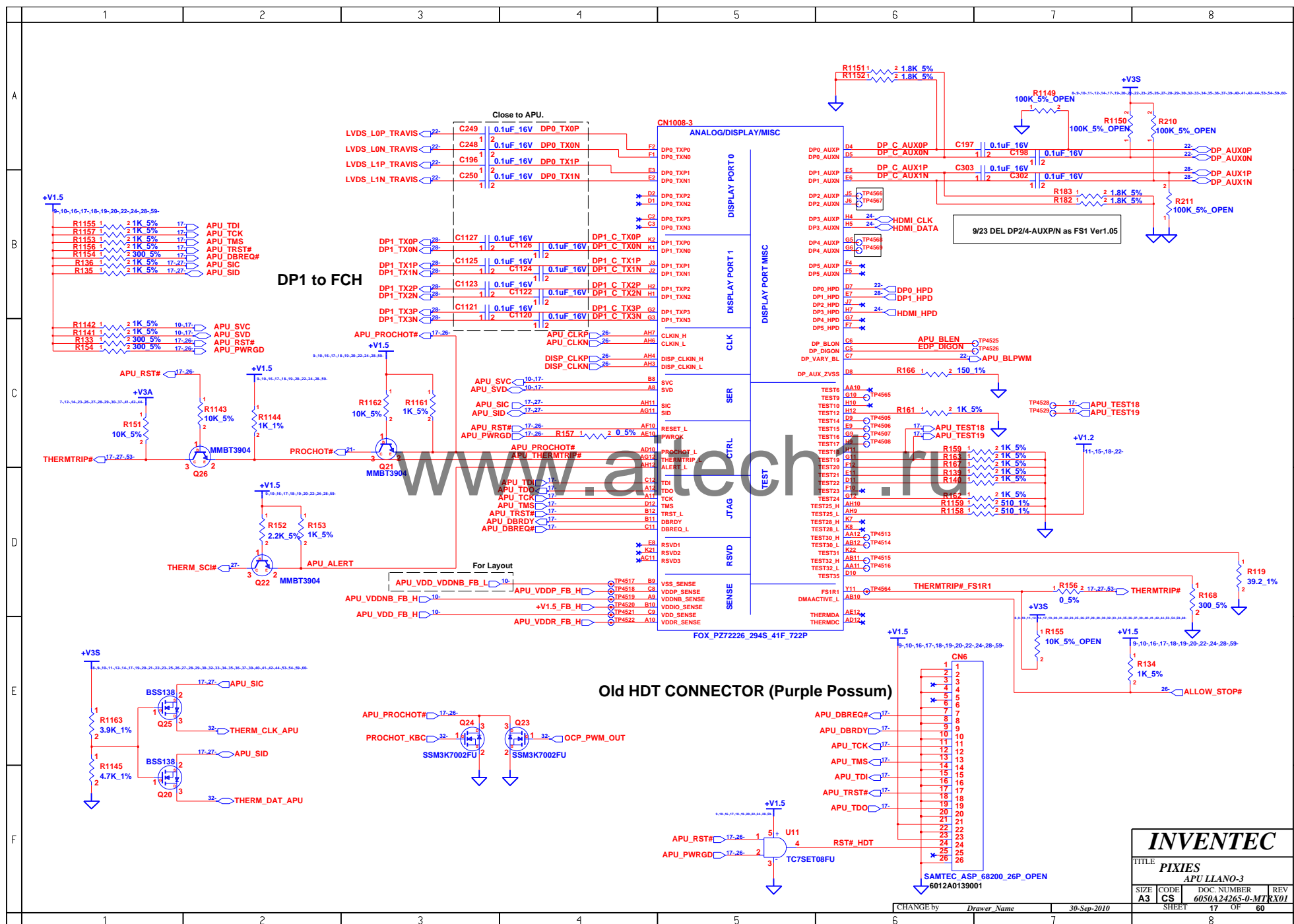


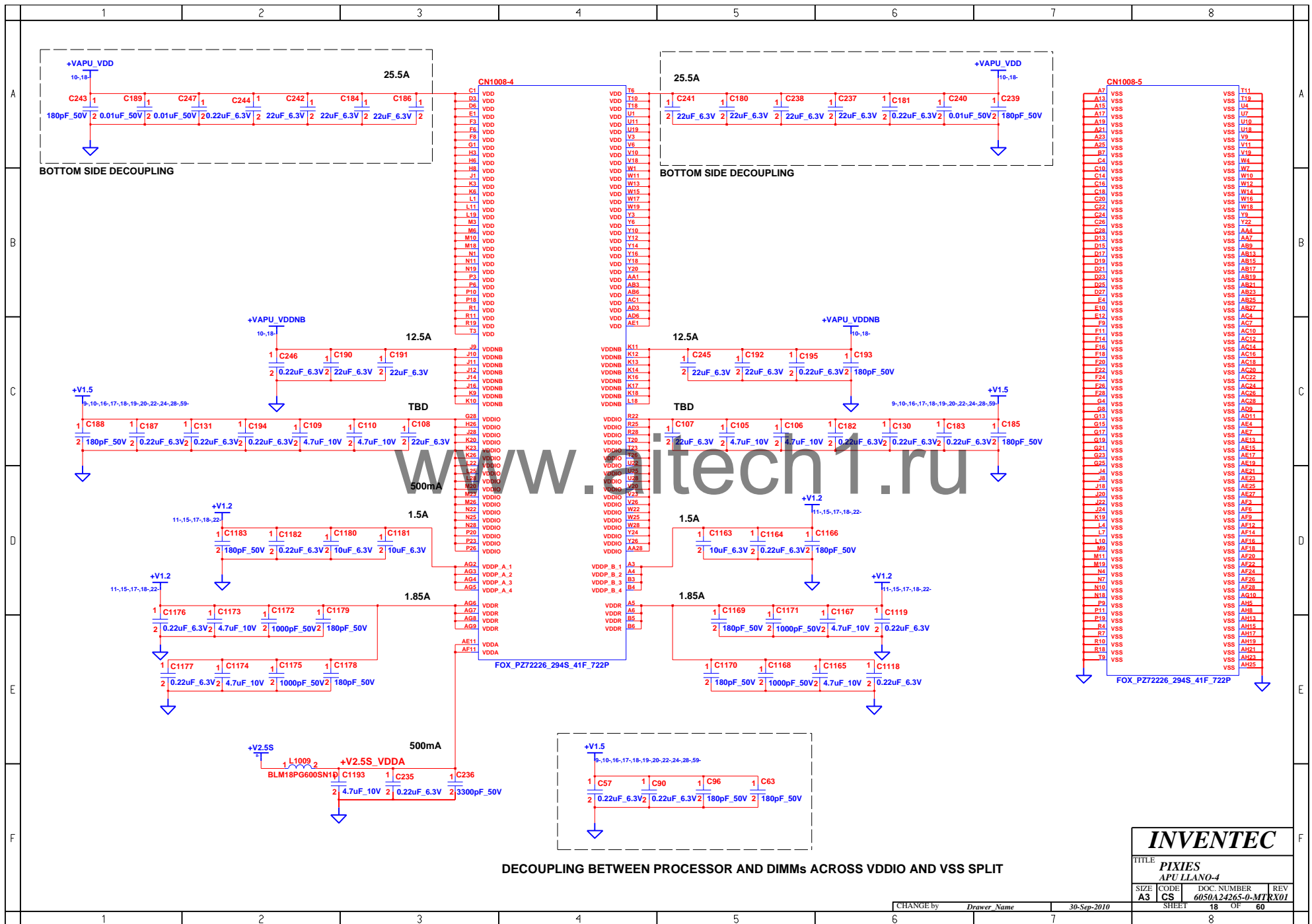


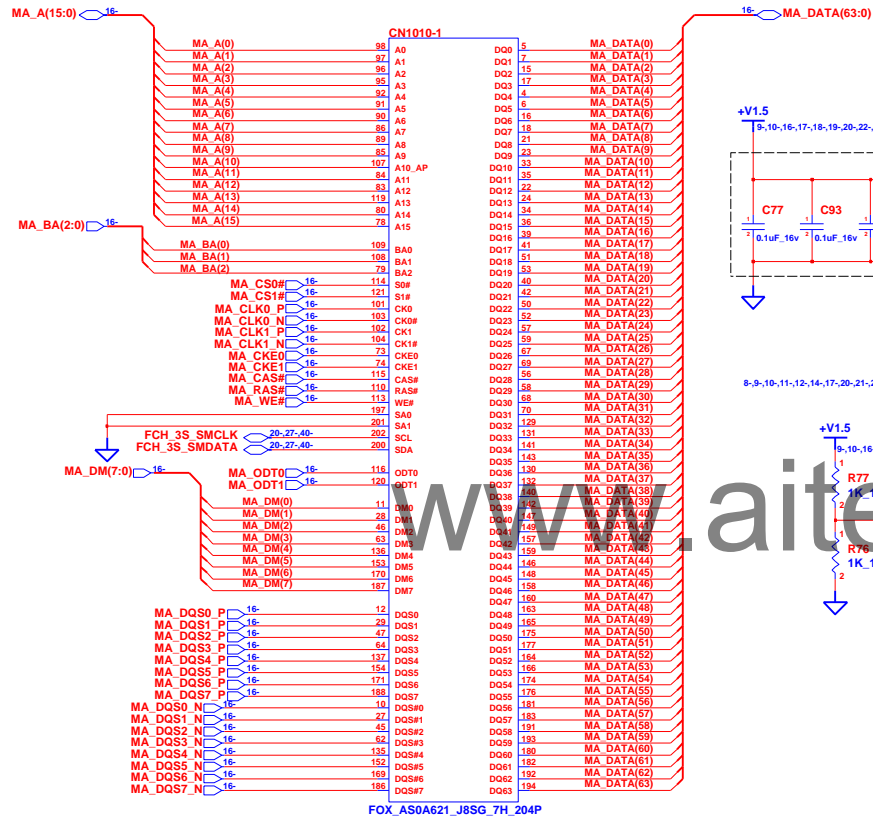
INVENTEC			
TITLE PIXIES POWER(SEQUENCE)			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
CHANGE by Drawer Name		30-Sep-2010	SHEET 14 OF 60



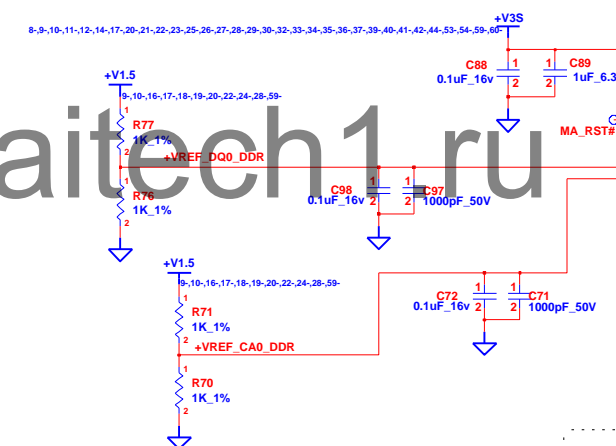
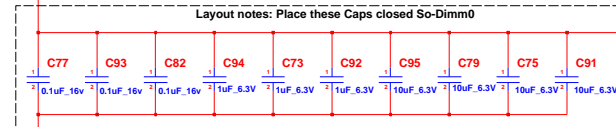








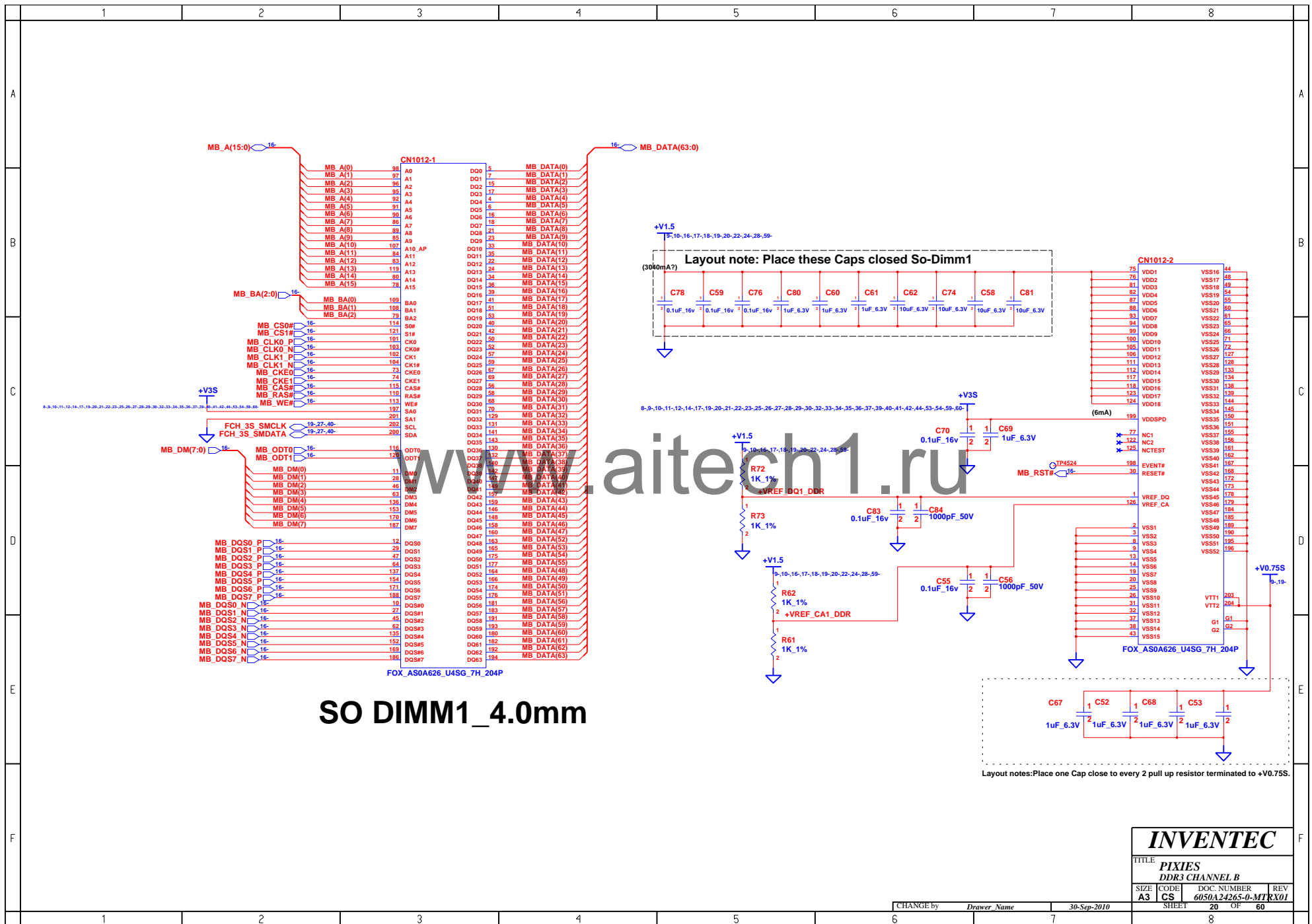
SO DIMM0_8.0mm

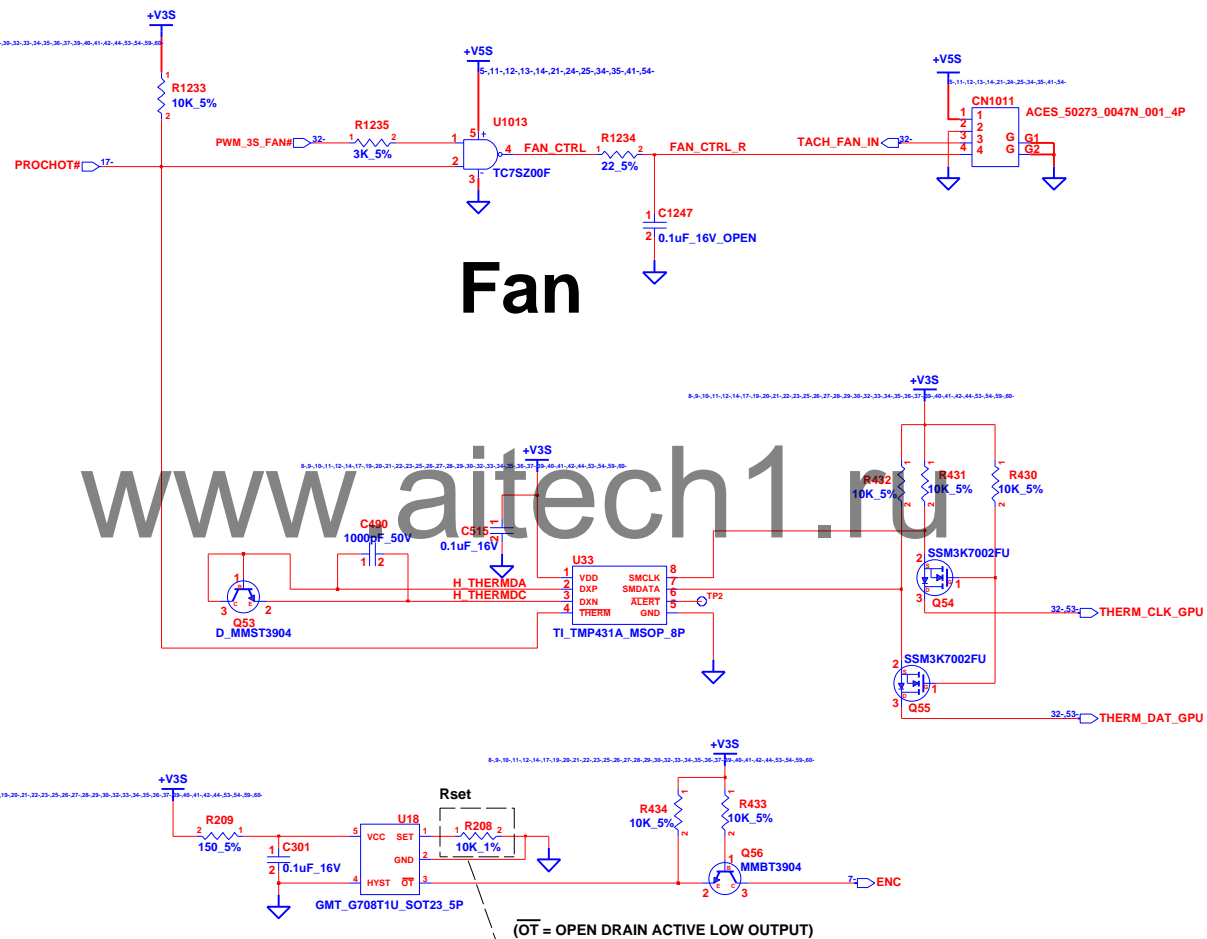


INVENTEC

TITLE PIXIES DDR3 CHANNEL A			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 1
SHEET 19		OF 60	

CHANGE by *Drawer Name* 30-Sep-2010





Thermal shutdown at 110°C
 $R_{set} (K \text{ ohm}) = 0.0012T^2 - 0.9308T + 96.147$
 (85°C:24Kohm)

Temperature switch

INVENTEC			
TITLE PIXIES Thermal & Fan			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
SHEET		21	OF 60

CHANGE by *Drawer Name* 30-Sep-2010

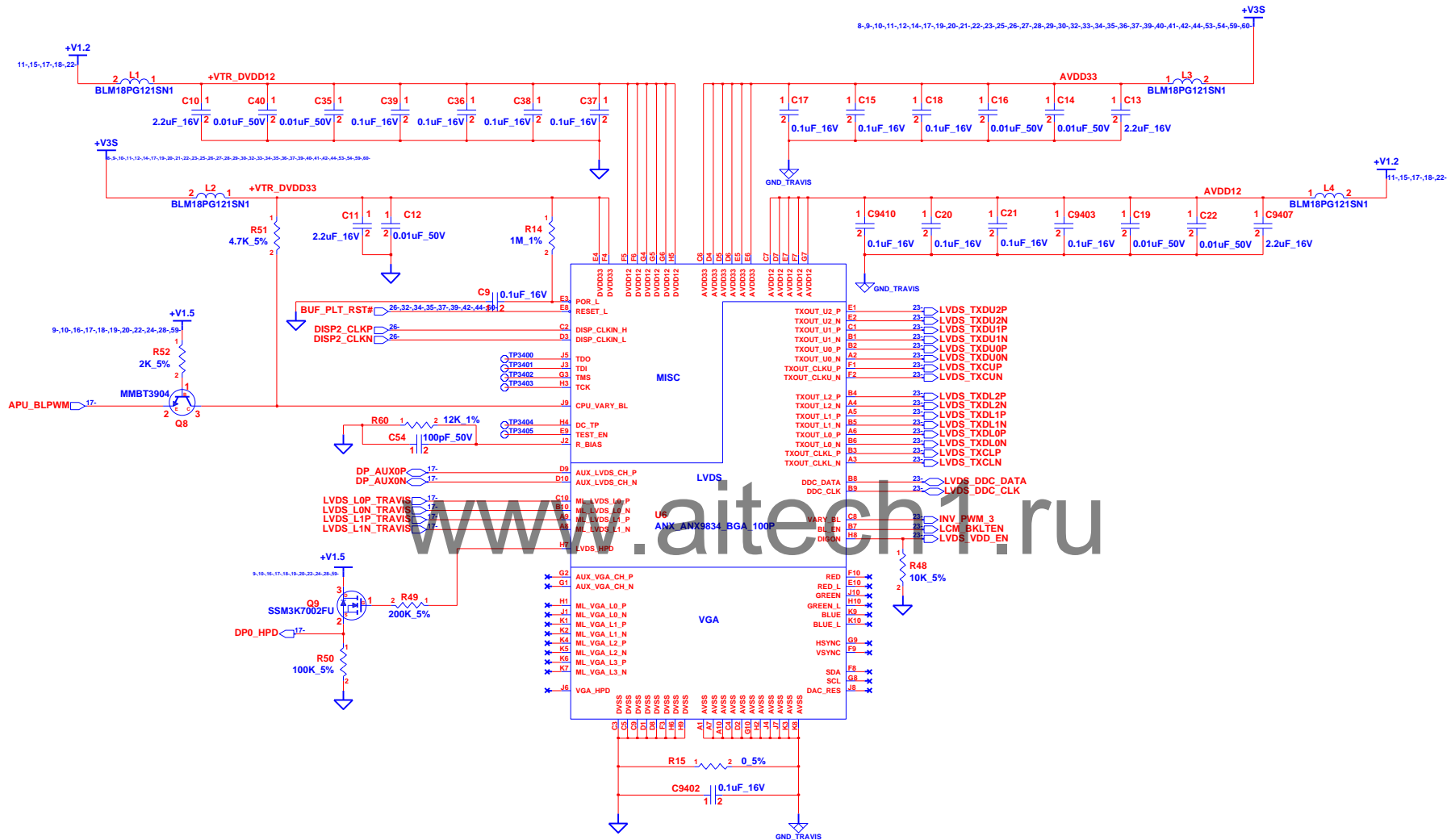
Travis (DP to LVDS)

INVENTEC

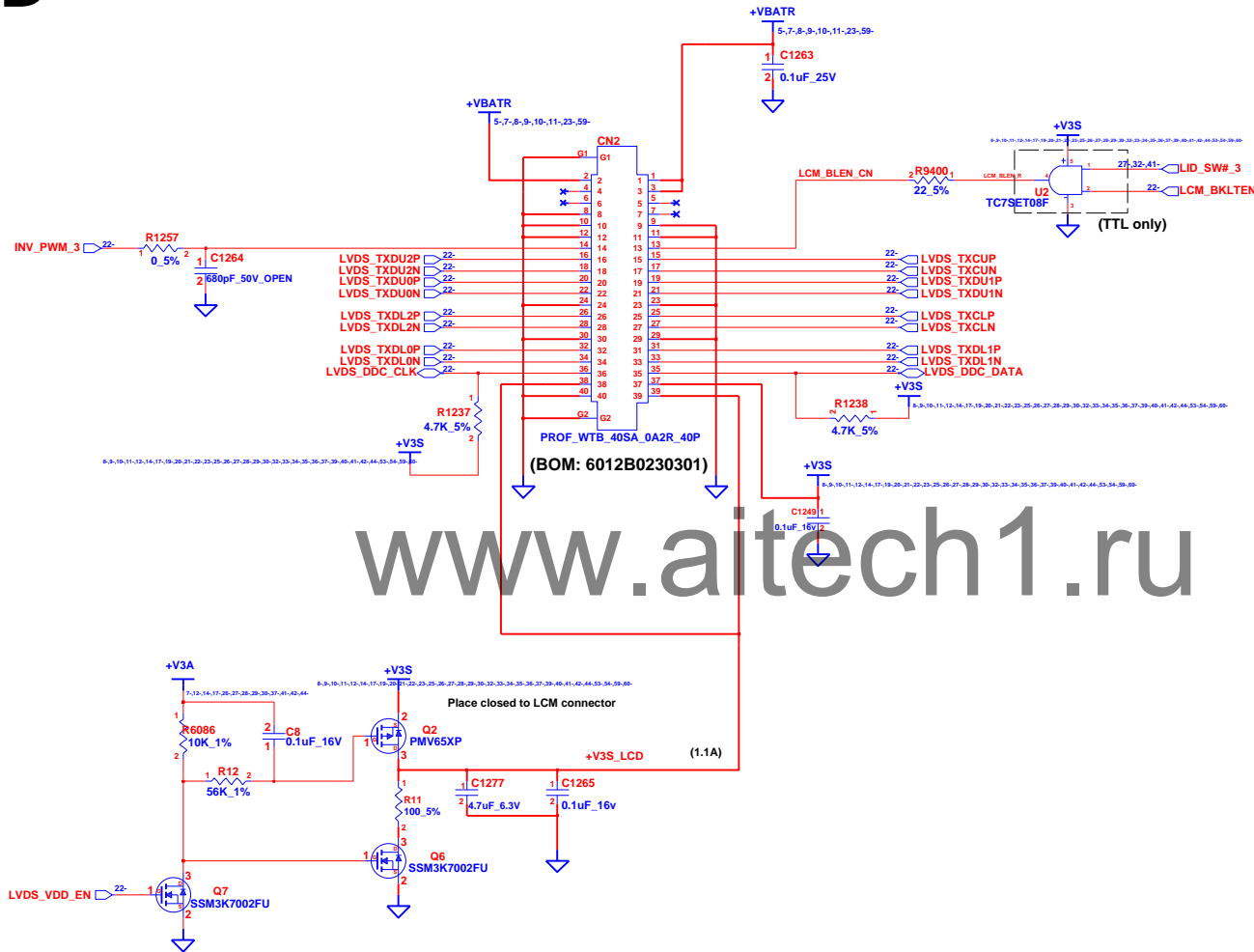
TITLE			
PIXIES			
Travis (DP to LVDS)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTR	01

CHANGE by *Drawer Name* 30-Sep-2010

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LCD

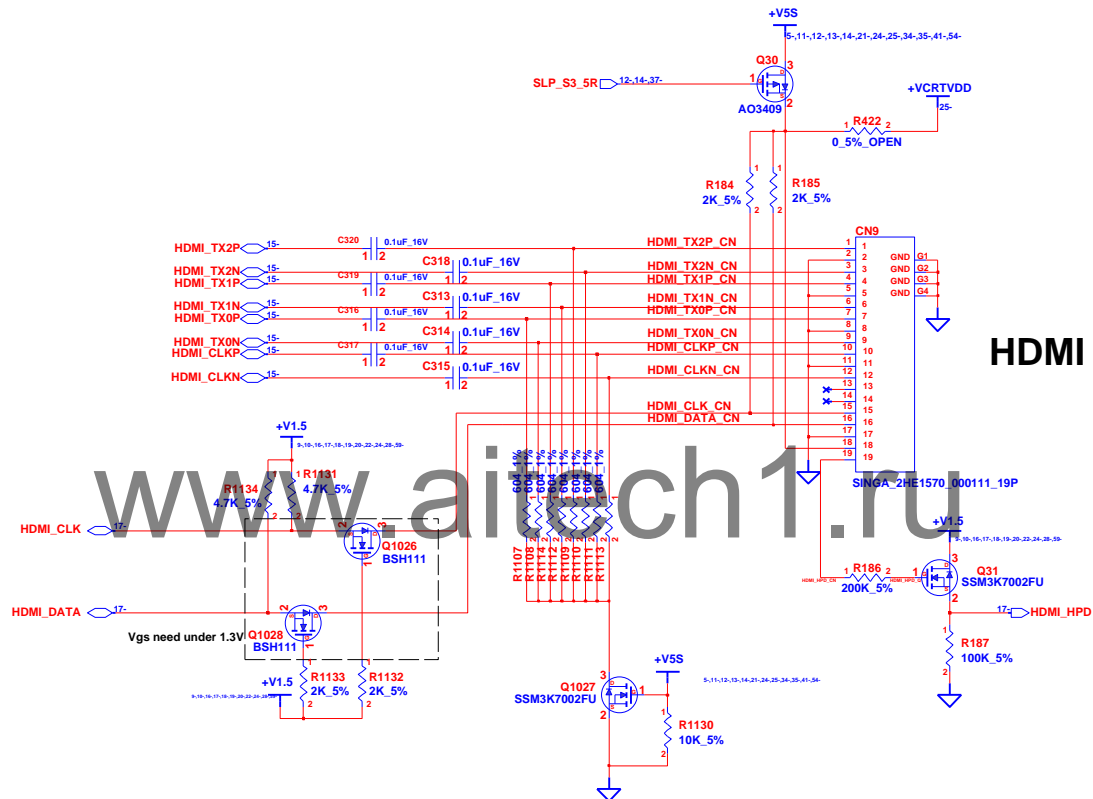


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INVENTEC

TITLE PIXIES LVDS			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 01
SHEET 23		OF 60	

CHANGE by *Drawer Name* 30-Sep-2010

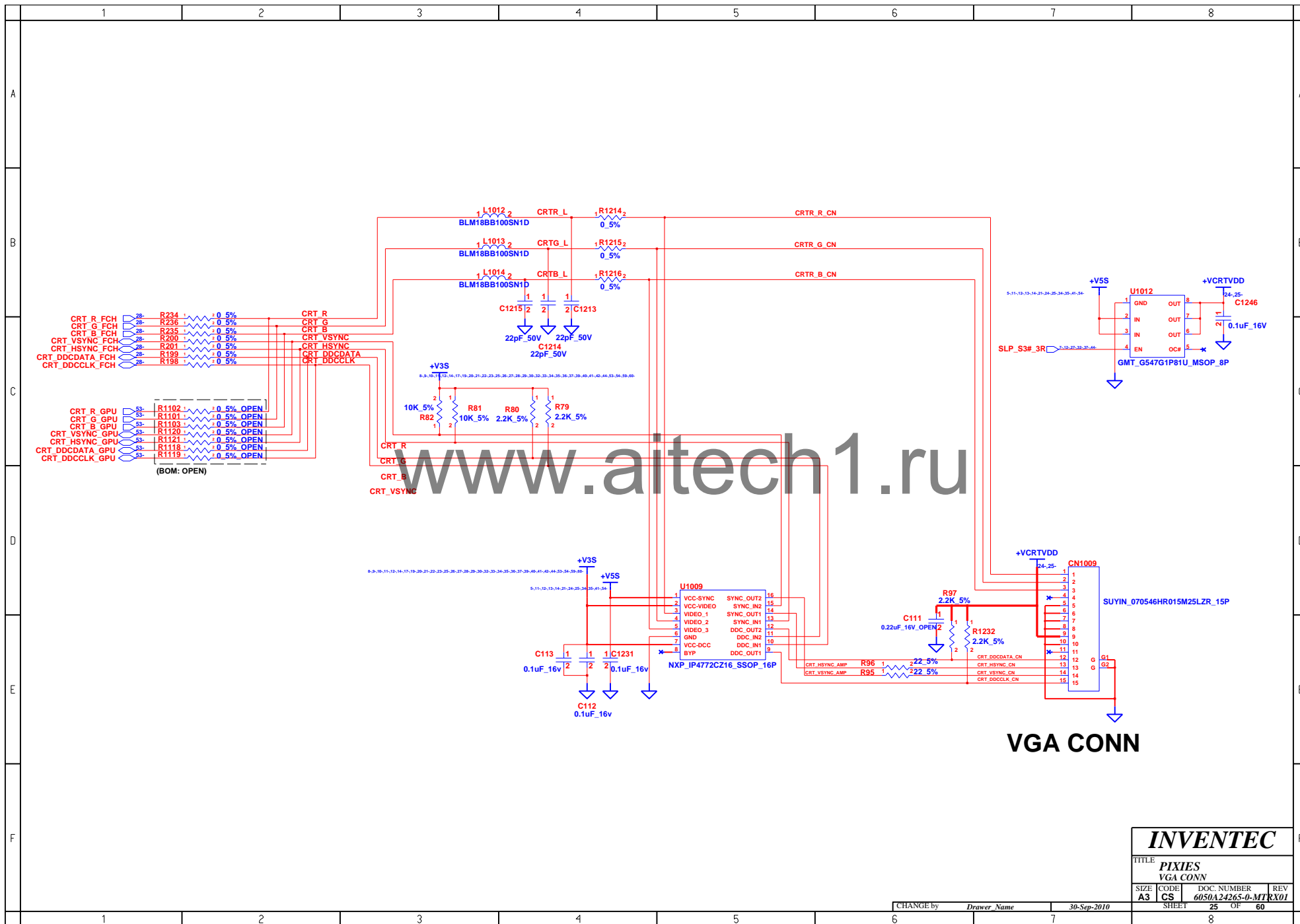


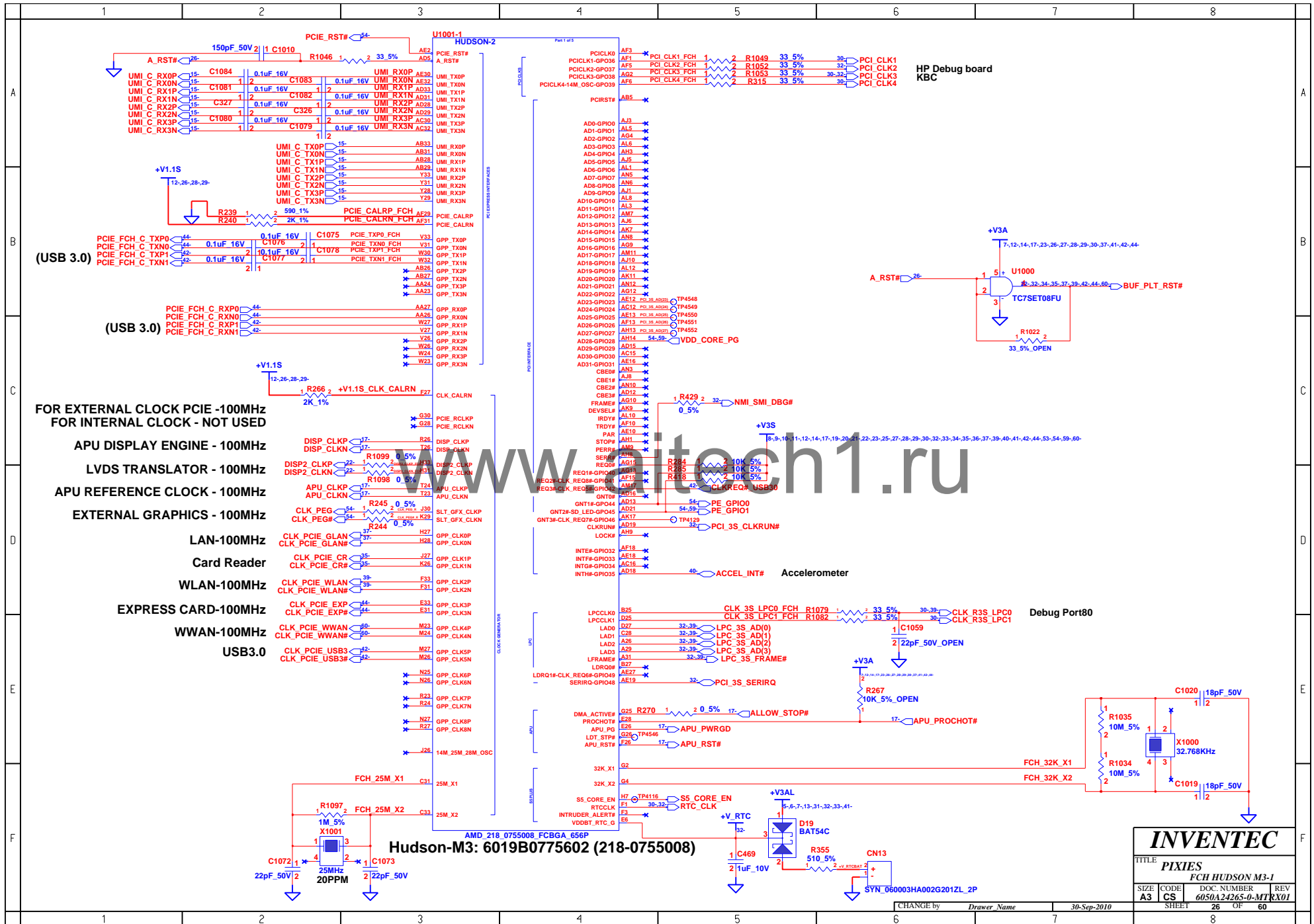
HDMI CONN

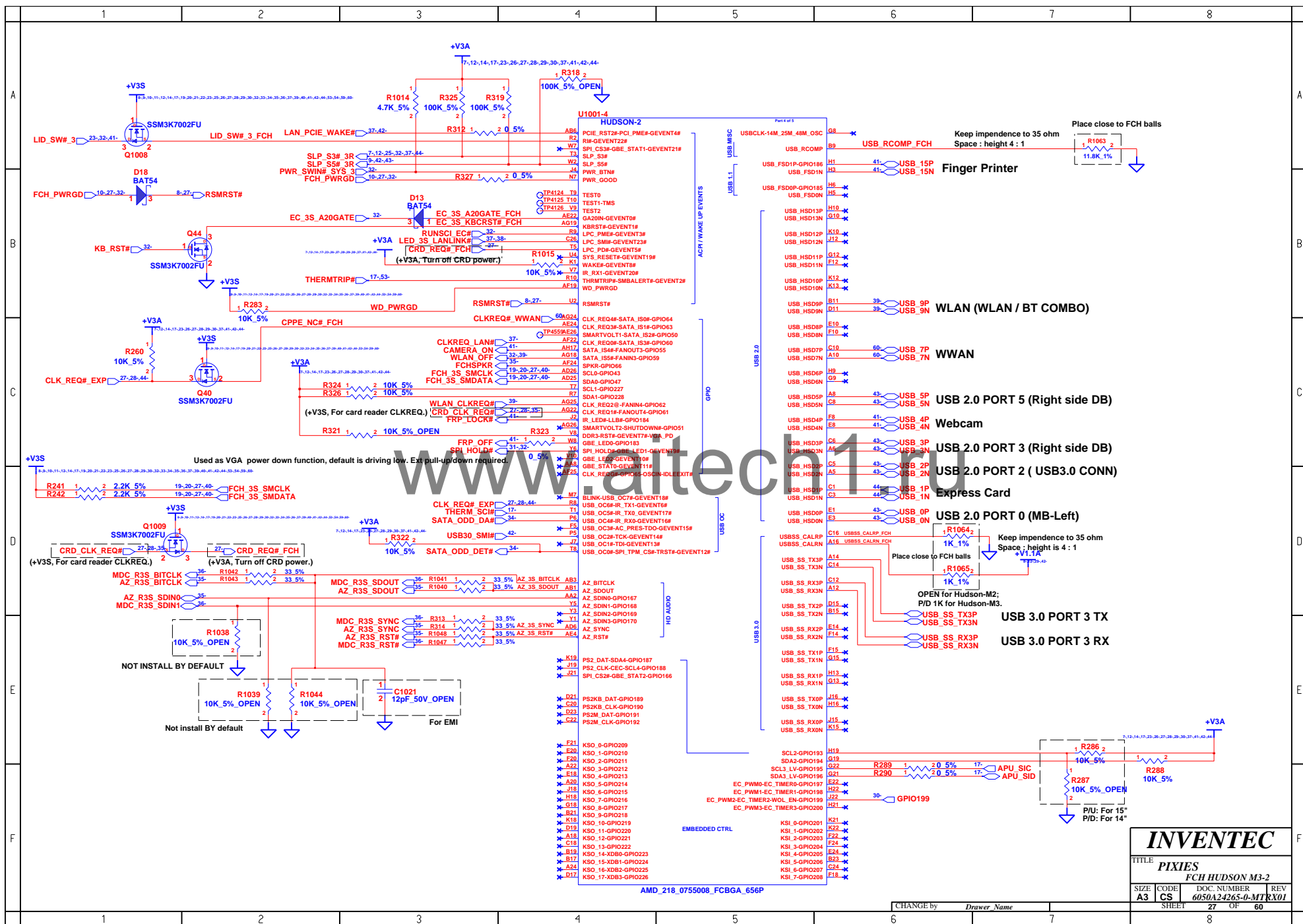
INVENTEC

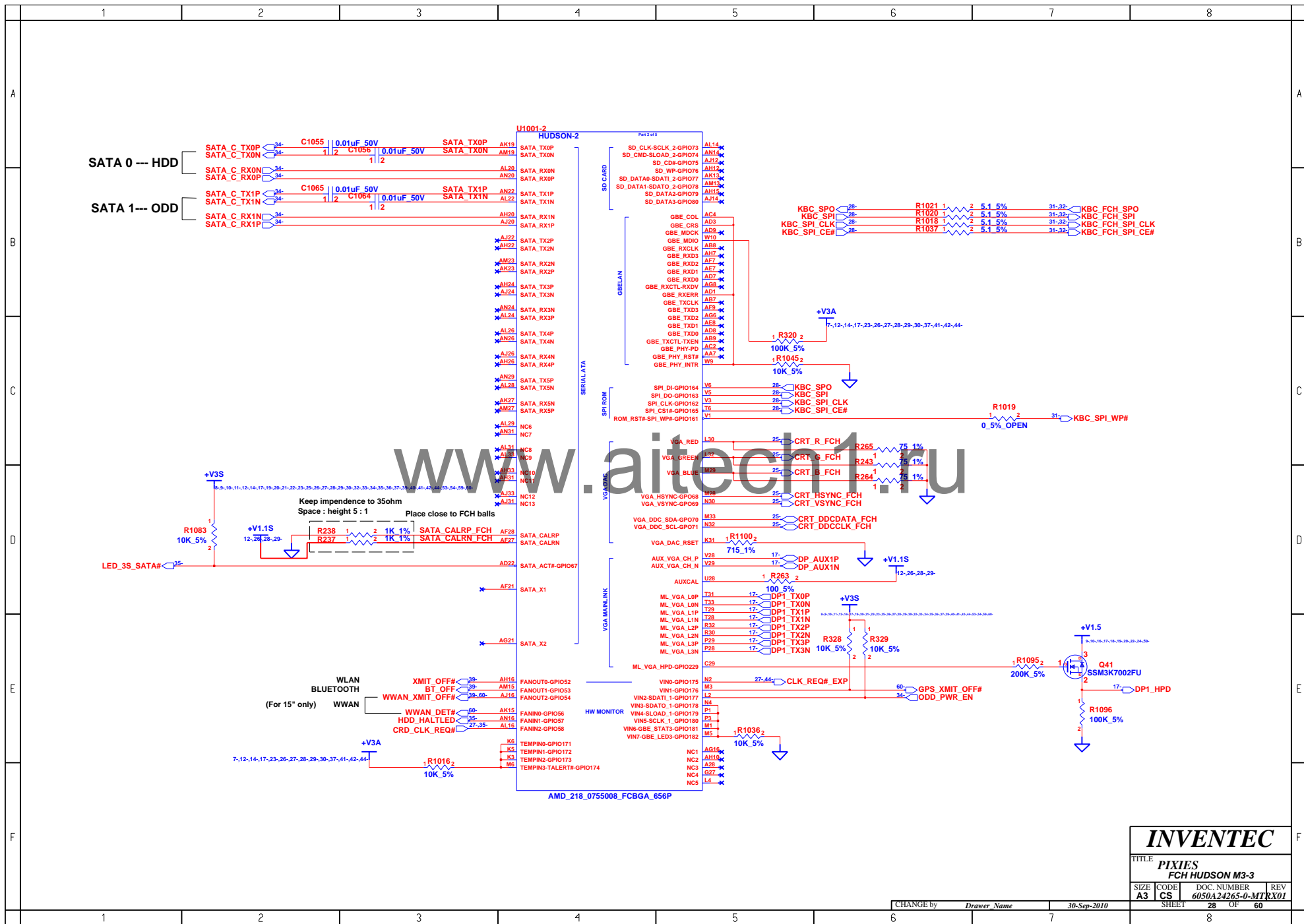
TITLE PIXIES HDMI CONN			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 1
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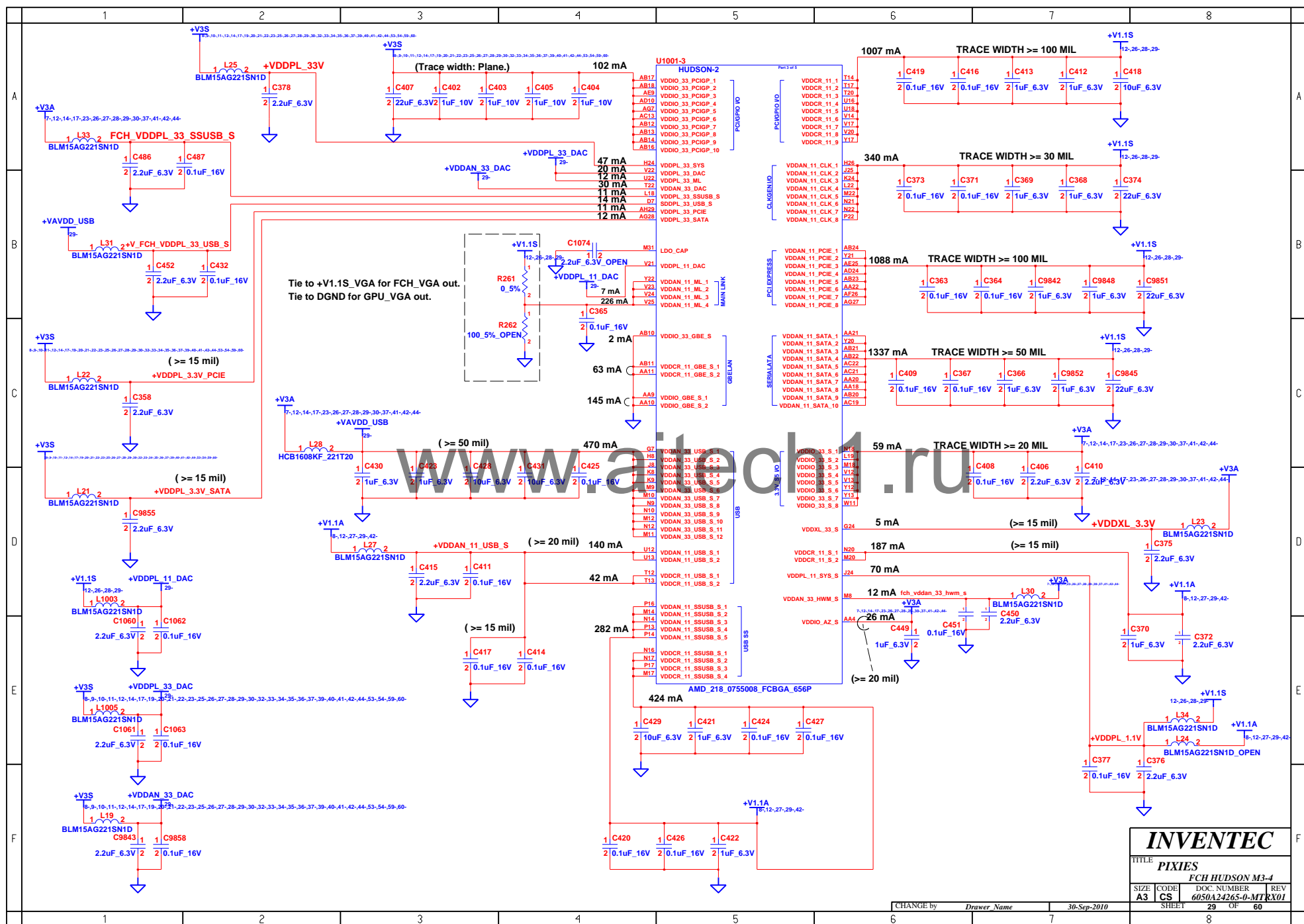
CHANGE by *Drawn Name* 27-Sep-2003

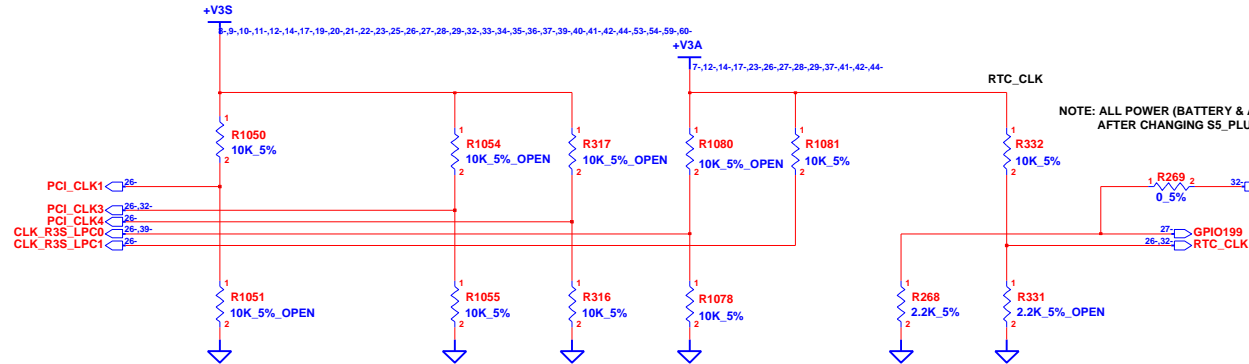












Required Straps:

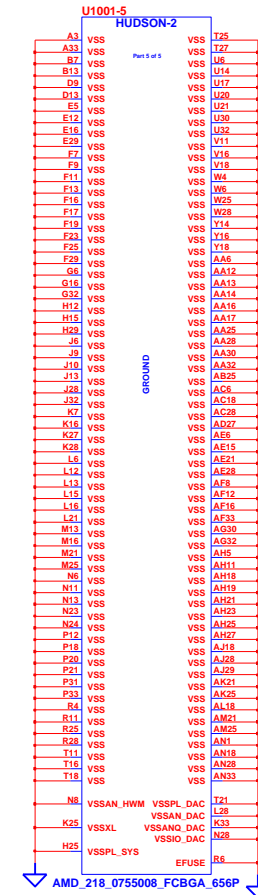
	PCI_CLK1		PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	(GPIO199) EC_PWM2	RTC_CLK
PULL HIGH	ALLOW PCIe Gen2 (default)		USE DEBUG STRAP	non_Fusion CLOCK MODE	EC ENABLED	CLKGEN ENABLED (Internal) (default)	LPC ROM	S5 Plus mode DISABLE (default)
PULL LOW	FORCE PCIe Gen1		IGNORE DEBUG STRAP (default)	FUSION CLOCK MODE (default)	EC DISABLED (default)	CLKGEN DISABLED	SPI ROM (default)	S5 Plus mode ENABLE

(PCI_3S_AD(27..23): No connected.)

Debug Straps

FCH HAS 15K INTERNAL PU FOR PCI_AD[27:23]

	PCI_3S_AD(27)	PCI_3S_AD(26)	PCI_3S_AD(25)	PCI_3S_AD(24)	PCI_3S_AD(23)
PULL HIGH	USE PCI PLL (default)	DISABLE ILA AUTORUN (default)	USE FC PLL (default)	USE DEFAULT PCIE STRAPS (default)	DISABLE PCI MEM BOOT (default)
PULL LOW	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT



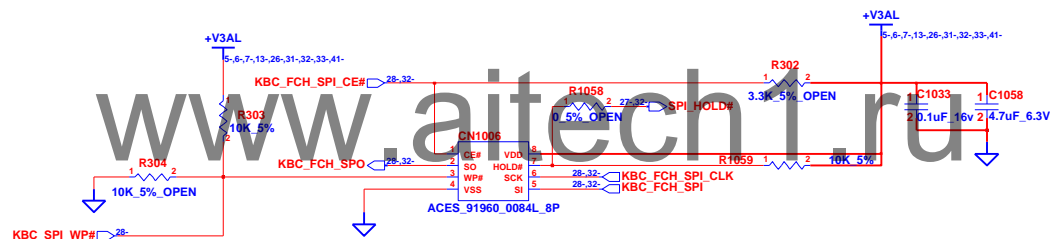
INVENTEC

TITLE
PIXIES
FCH HUDSON M3-5

SIZE CODE DOC. NUMBER REV
A3 CS 6050A24265-0-MTRX01

CHANGE by *Drawer Name* 30-Sep-2010

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SPI Flash

SPI Socket P/N:6026B0150101

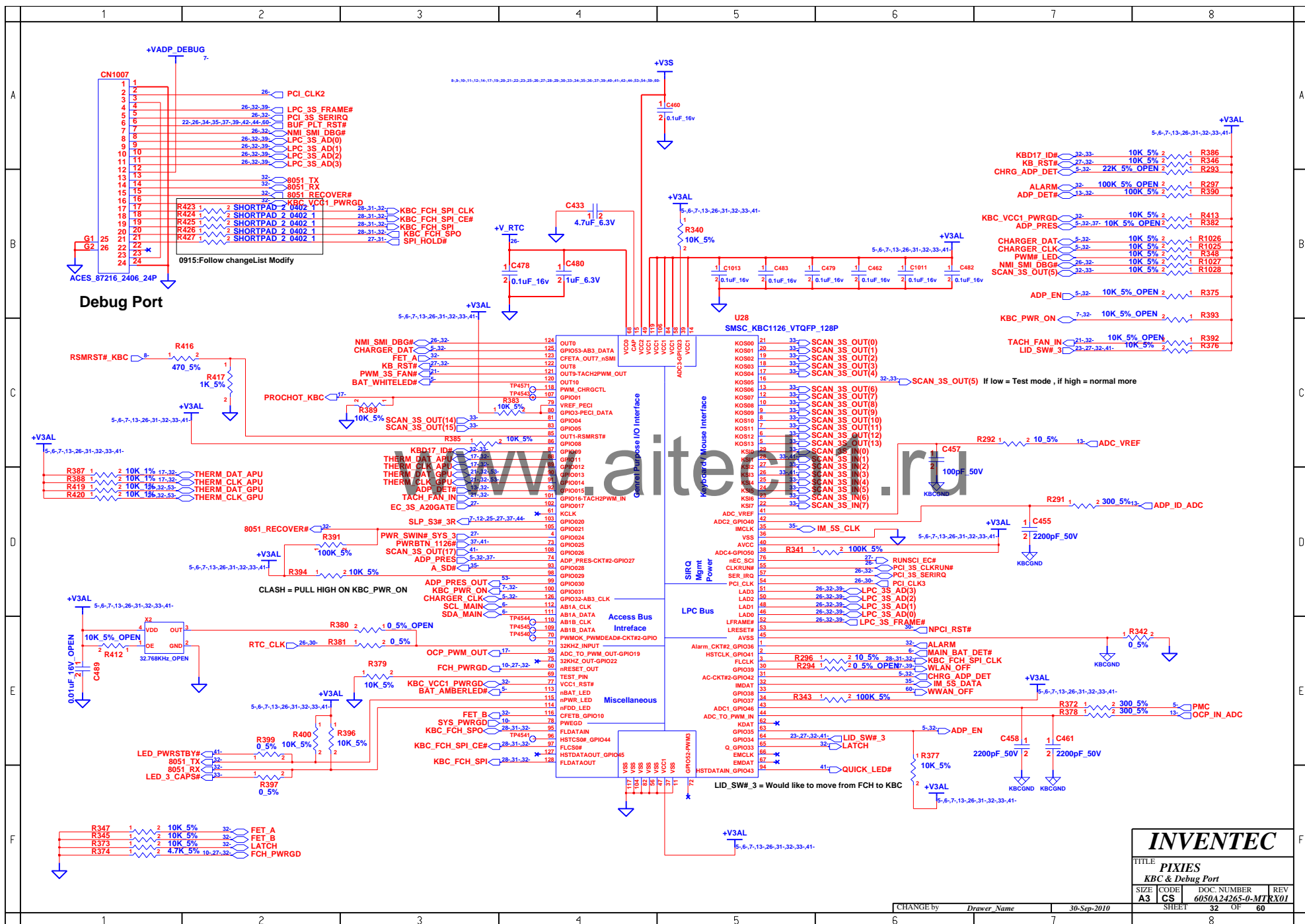
SPI ROM P/N:6019B0614401

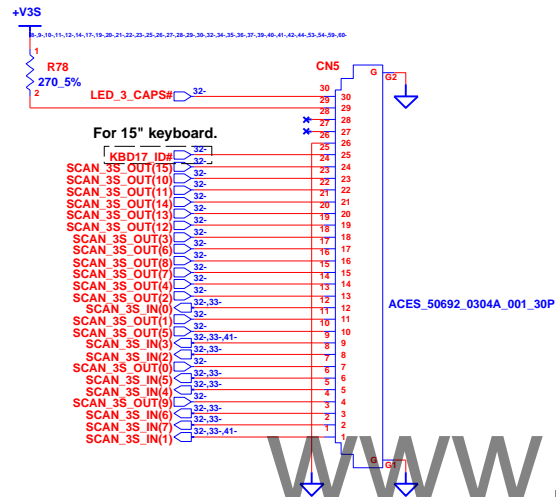
INVENTEC

TITLE PIXIES BIOS ROM (SPI)			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 01

CHANGE by *Drawer Name* 30-Sep-2010

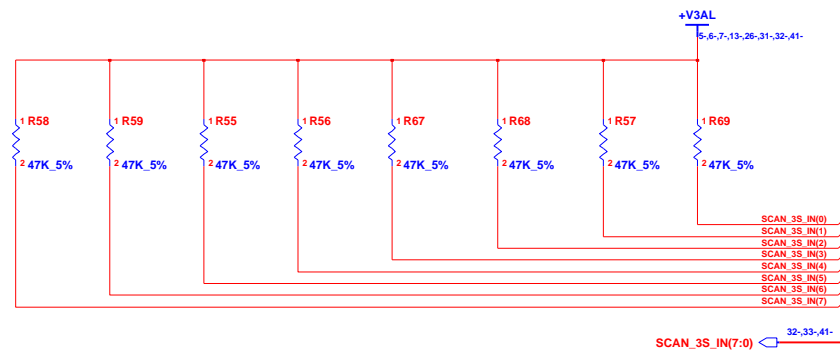
SHEET 31 OF 60





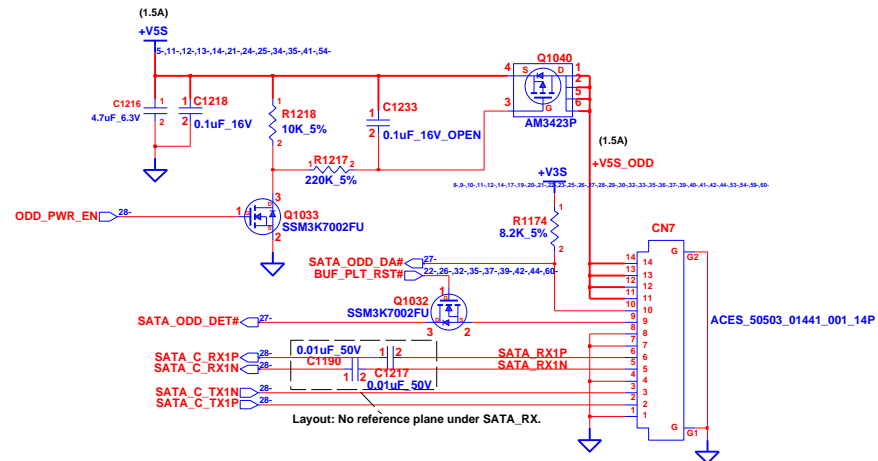
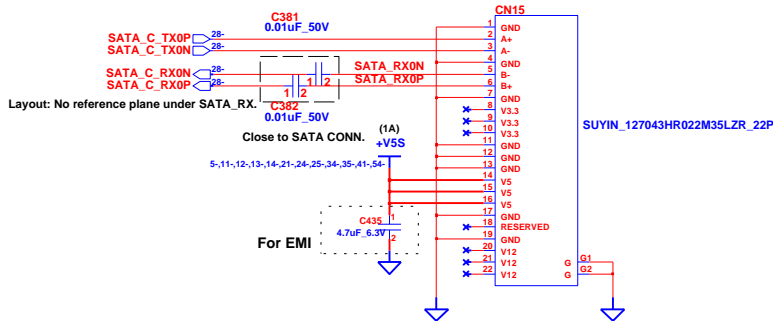
9/9 : Removed T/P CONN CN1000

Keyboard CONN



INVENTEC

TITLE PIXIES Keyboard & TouchPad CONN			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 01
CHANGE by <i>Drawer Name</i>		30-Sep-2010	
SHEET 33		OF 60	

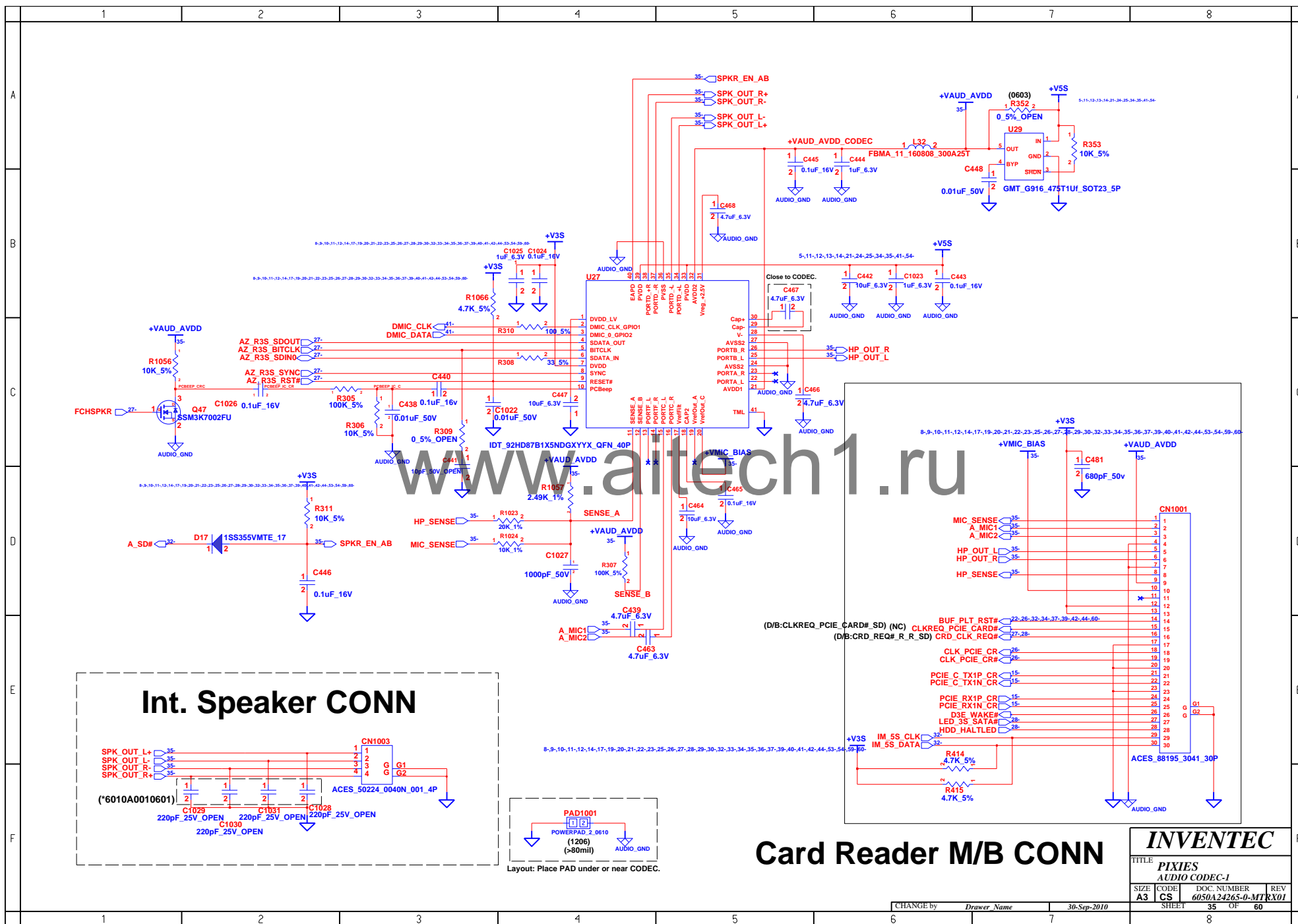


SATA HDD

SATA ODD

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INVENTEC			
TITLE			
PIXIES			
SATA HDD & ODD CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTRX01	
CHANGE by		Drawer Name	30-Sep-2010
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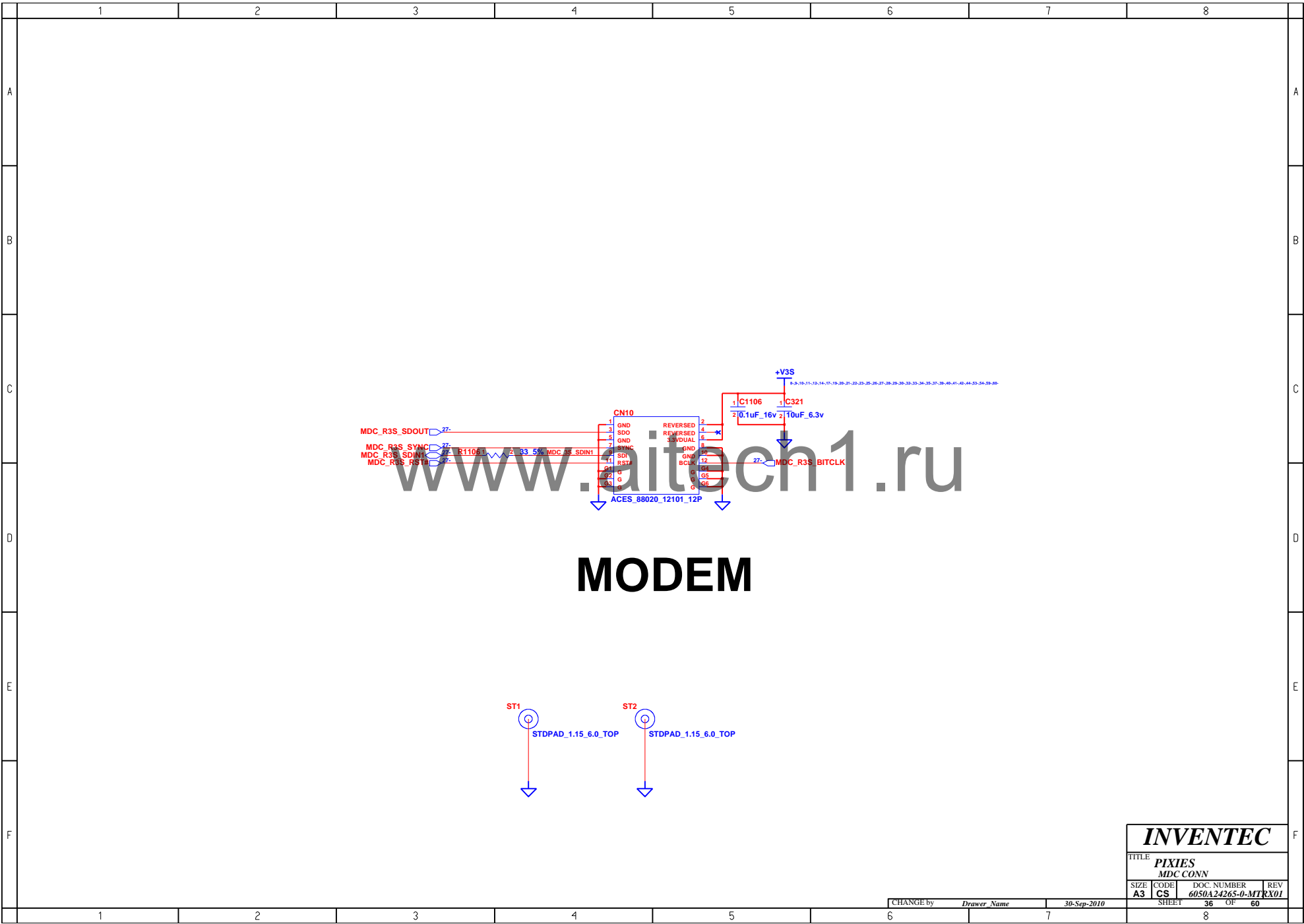


Card Reader M/B CONN

INVENTEC

TITLE PIXIES AUDIO CODEC-1			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV REV
SHEET 35		OF 60	

CHANGE by Drawer Name 30-Sep-2010



MODEM

INVENTEC

TITLE
PIXIES
MDC CONN

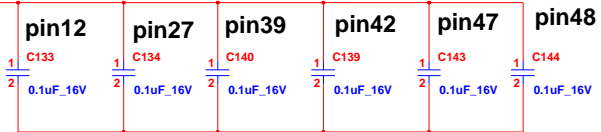
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 01
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CHANGE by *Drawer Name* 30-Sep-2010

SHEET 36 OF 60

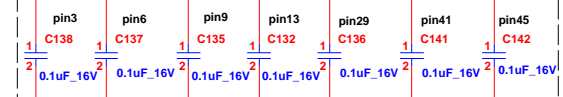
+V3_LAN rising time: 1ms ~ 100ms.

Placed near LAN Controller



Placed near LAN Controller

Placed near LAN Controller



For pin21



INVENTEC

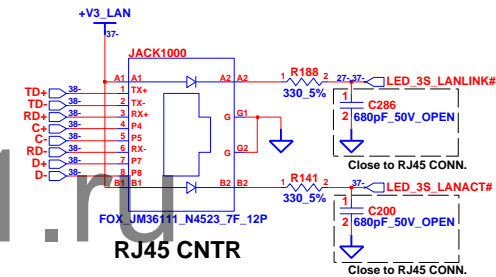
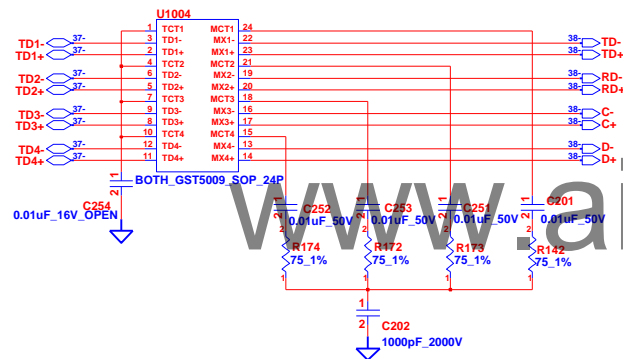
TITLE PIXIES Giga LAN Controller			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 01

CHANGE by *Drawer Name* 30-Sep-2010

SHEET 37 OF 60

LAYOUT NOTE:

To place one 0.1uF at each pin 1 , 4 , 7 , 10
and place the 1uF in the spot that
is as close as possible to all 4 pins



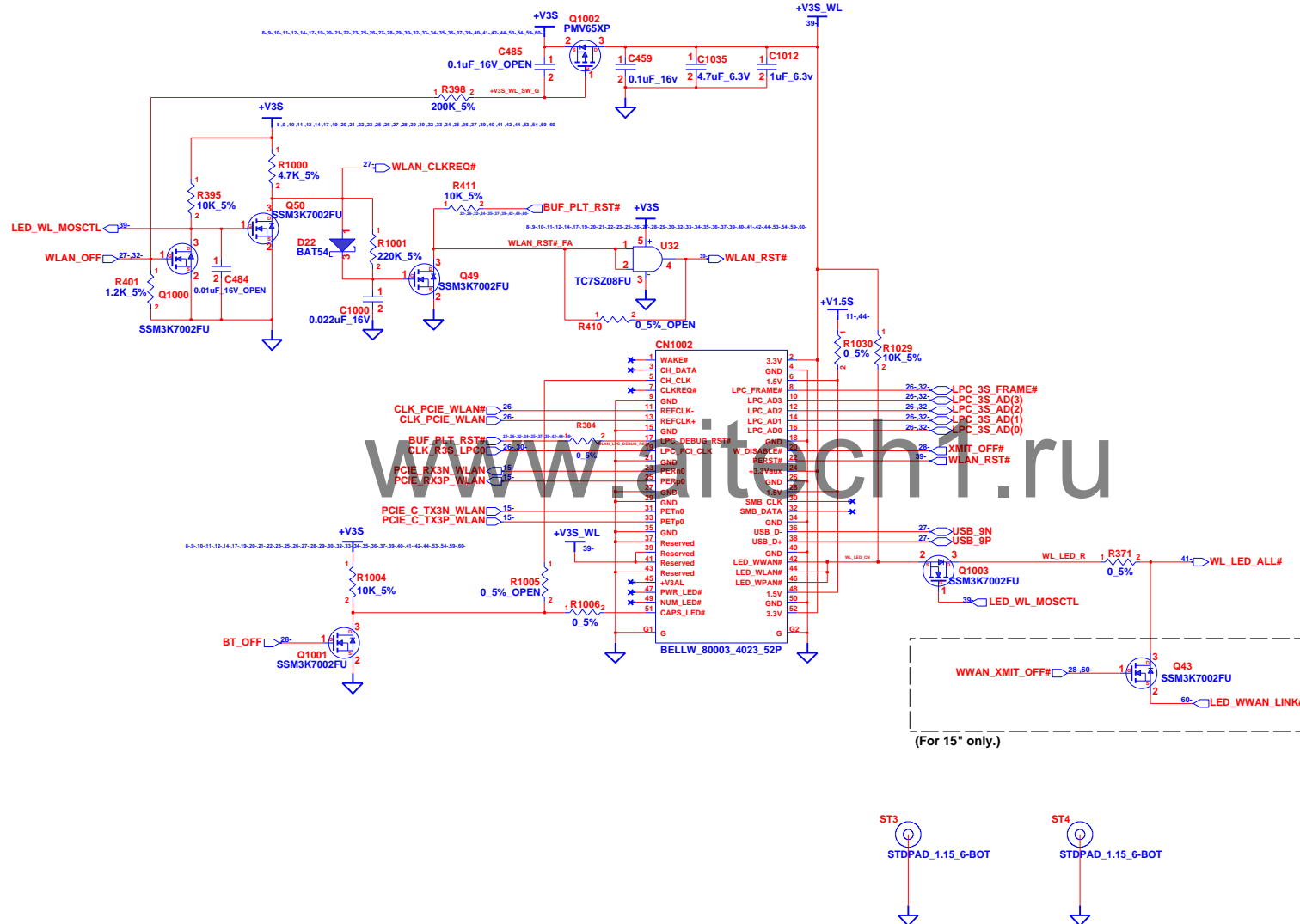
INVENTEC

TITLE **PIXIES**
Transformer & RJ45 CONN

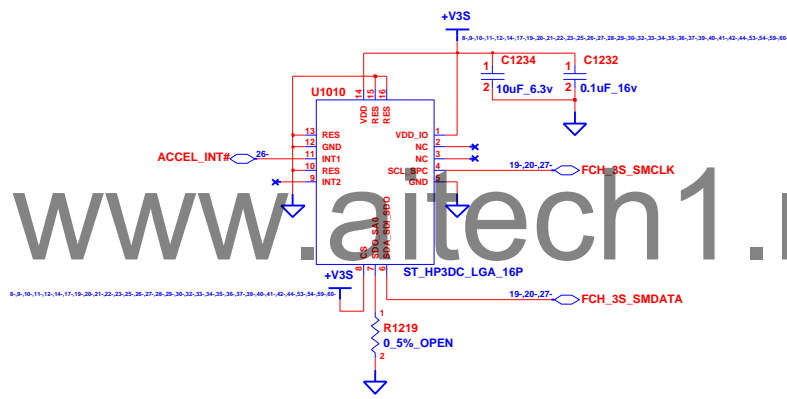
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTR	01

CHANGE by *Drawer Name* 30-Sep-2010

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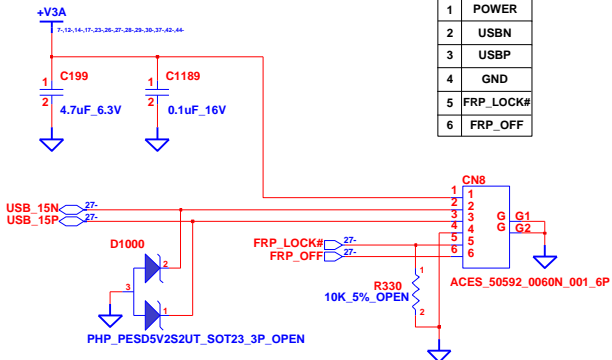


INVENTEC			
TITLE PIXIES MINI CARD (WLAN)			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 1
CHANGE by Drawer Name		30-Sep-2010	SHEET 39 OF 60

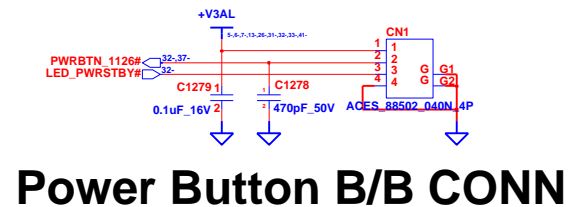


Accelerometer

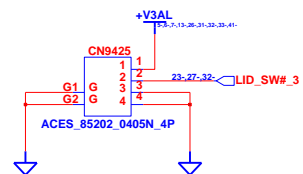
INVENTEC			
TITLE PIXIES ACCELEROMETER			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV
CHANGE by Drawer Name		30-Sep-2010	SHEET 40 OF 60



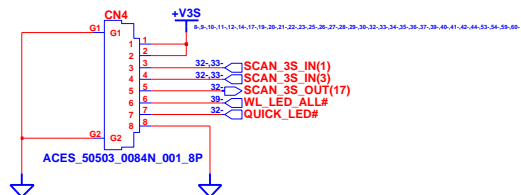
Finger Printer CONN



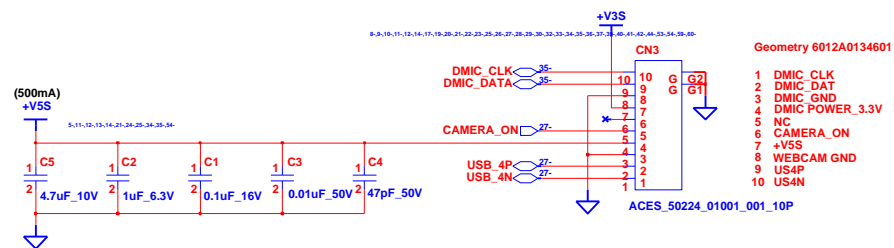
Power Button B/B CONN



LID SWITCH B/B CONN



Quick Launch B/B CONN



DMIC / WebCAM

INVENTEC			
TITLE PIXIES			
PWR&FP&Launch&Web/DMIC CONN			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV
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CHANGE by Drawer Name 30-Sep-2010

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USB3.0 Controller

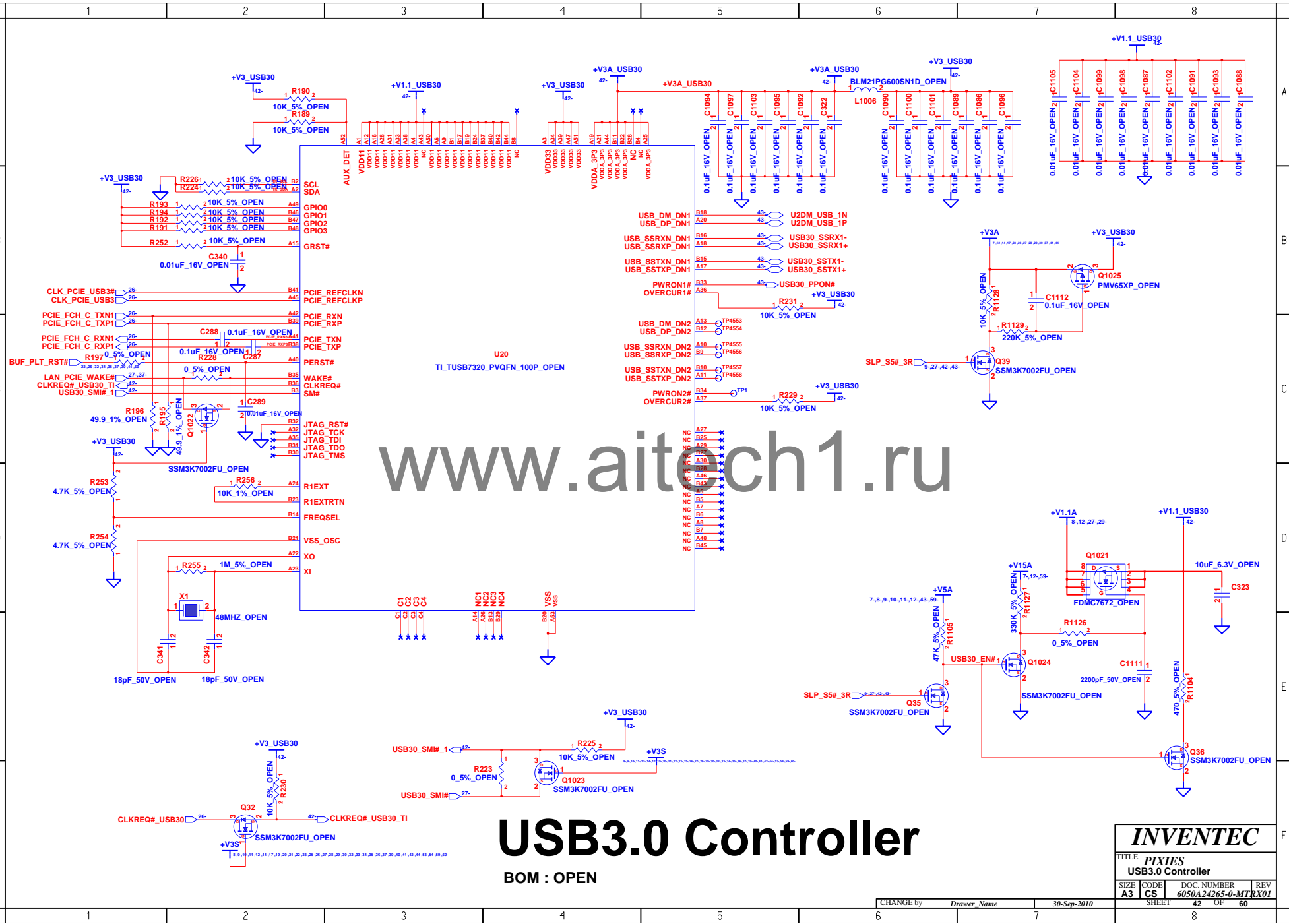
BOM : OPEN

INVENTEC

TITLE PIXIES
USB3.0 Controller

SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A24265-0-MTRX01	
SHEET		42	OF 60

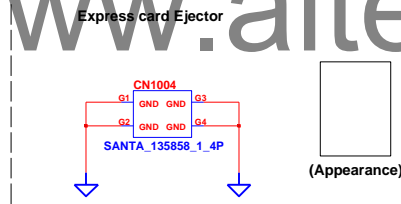
CHANGE by Drawer Name 30-Sep-2010





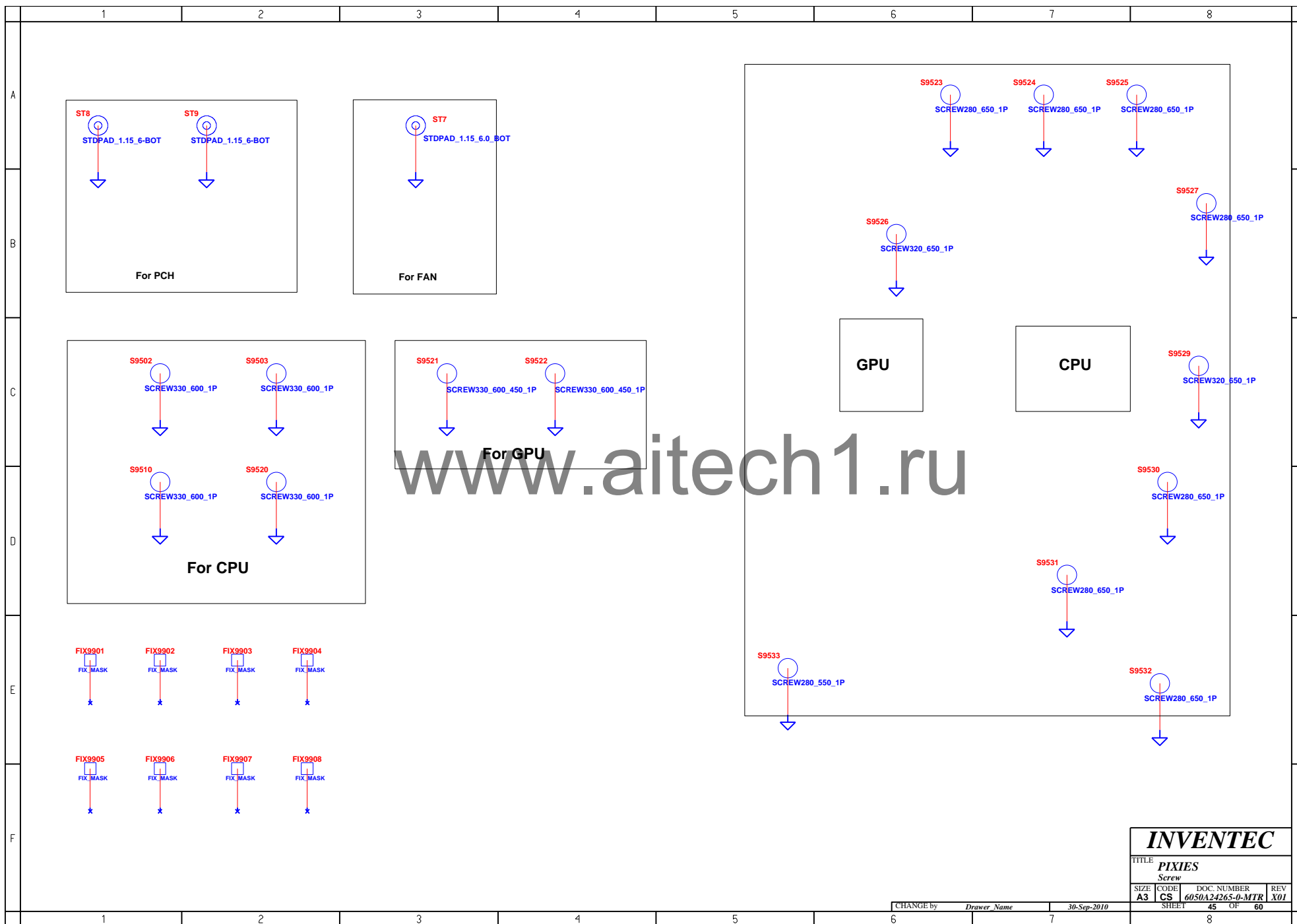
CHANGE by	<i>Drawer Name</i>	<i>30-Sep-2010</i>
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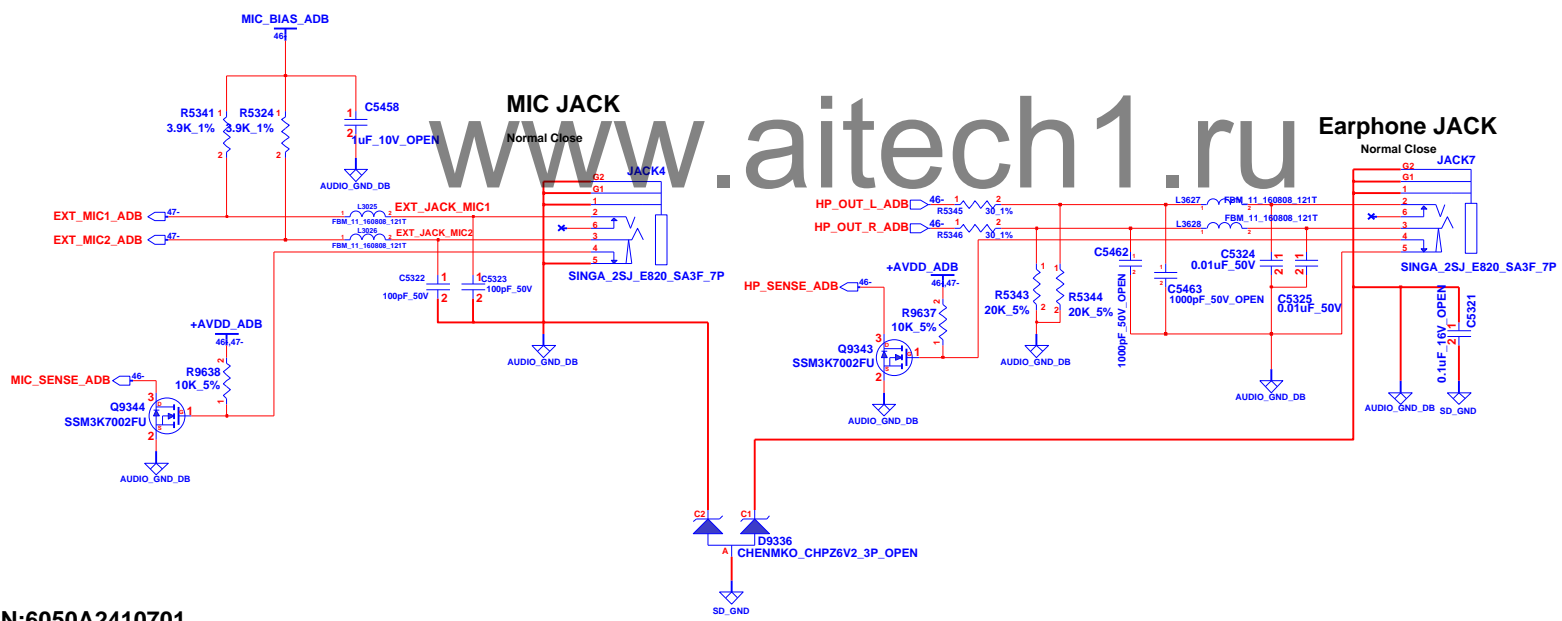
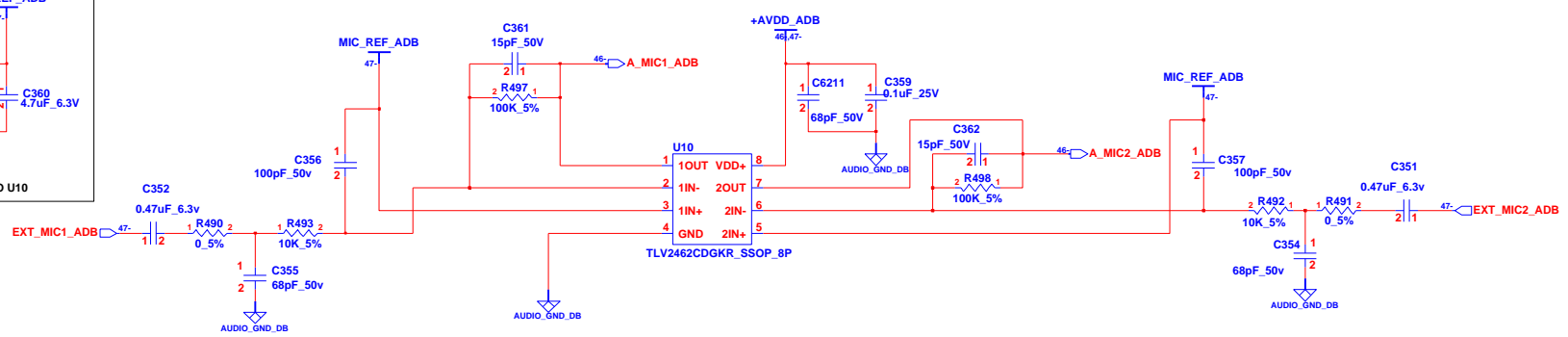
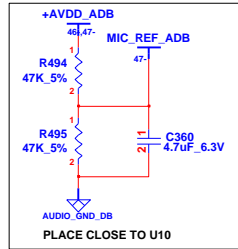
www.aitech1.ru



Express card

INVENTEC			
TITLE PIXIES Express Card			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 1
CHANGE by Drawer Name		30-Sep-2010	SHEET 44 OF 60





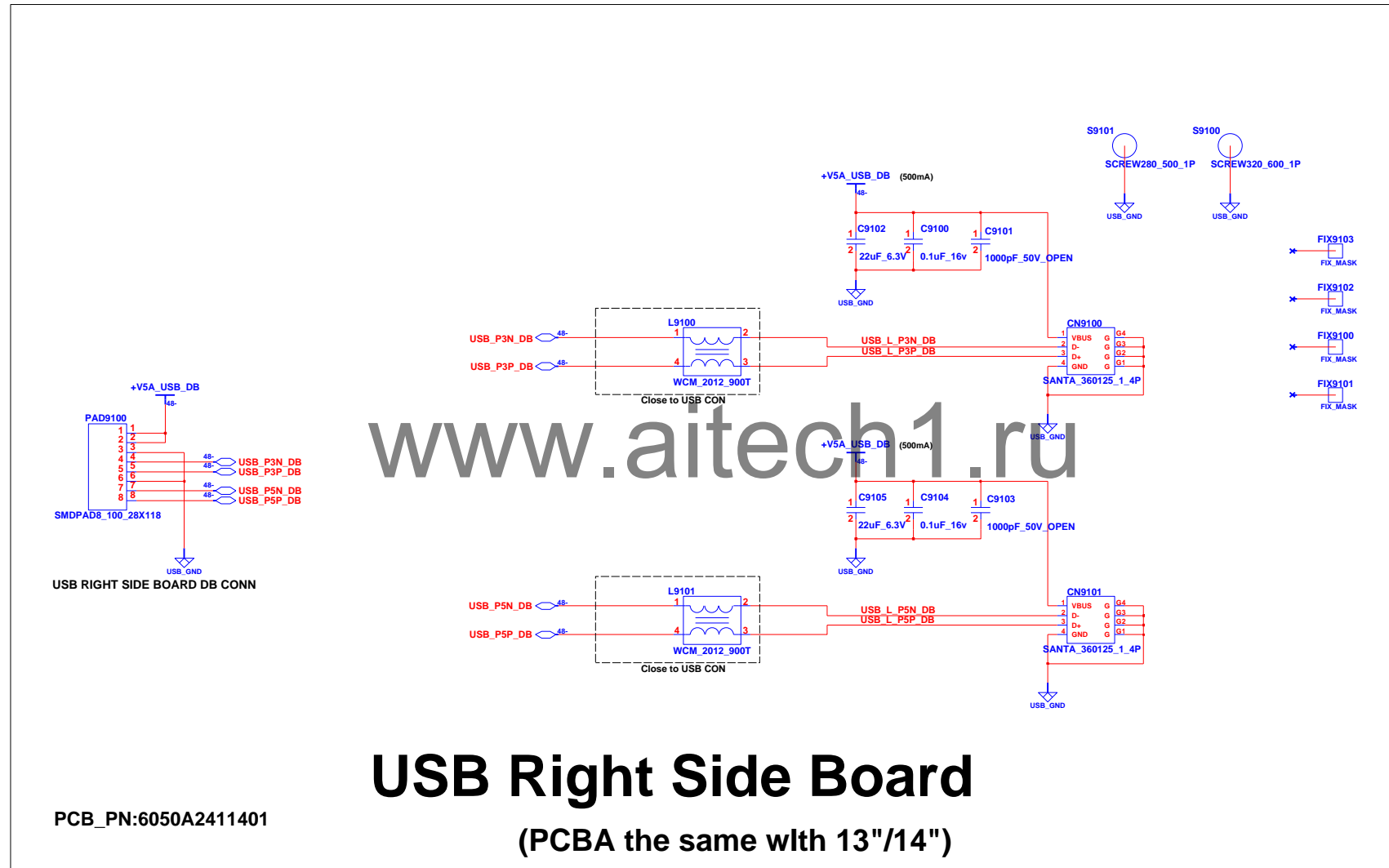
PCB_PN:6050A2410701

Card READER & AUDIO BOARD 2/2

INVENTEC

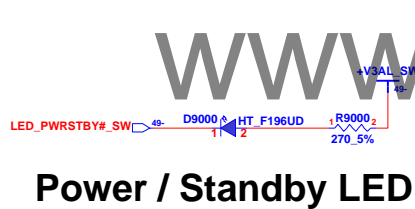
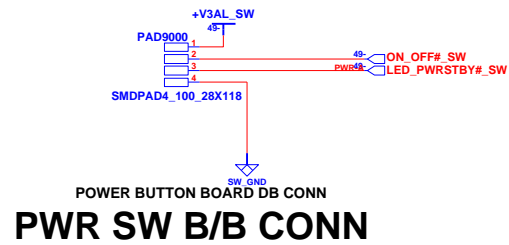
TITLE PIXIES			
3 in 1 Card Reader daughter board-2			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTRX01	REV 1
SHEET 47		OF 60	

CHANGE by *Drawer Name* 30-Sep-2010

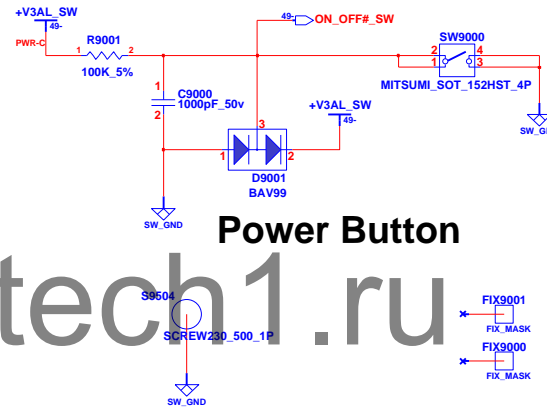


INVENTEC			
TITLE PIXIES			
USB Board (Right side)			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
SHEET 48		OF 60	

CHANGE by *Drawer Name* 30-Sep-2010



PCB_PN:6050A2411201



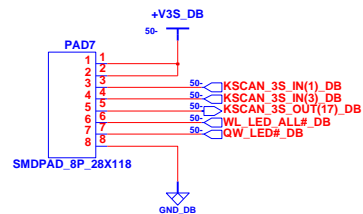
Power Button Board

(PCBA the same with 13"/14")

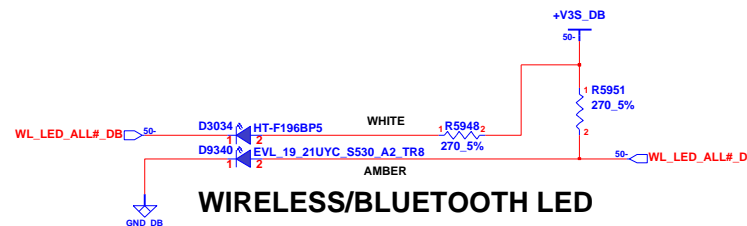
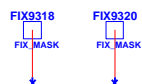
INVENTEC			
TITLE PIXIES Power Button Board			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
CHANGE by Drawer Name		30-Sep-2010	
SHEET		49	OF 60

Quick Launch Board

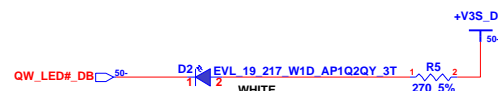
PCB_PN:6050A2410501



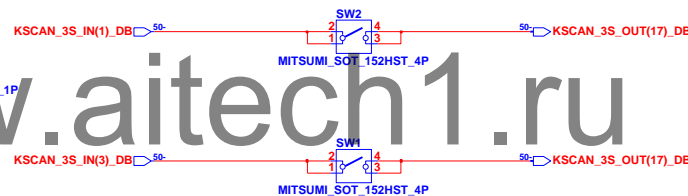
QUICK LAUNCH BOARD DB CONN



WIRELESS/BLUETOOTH LED



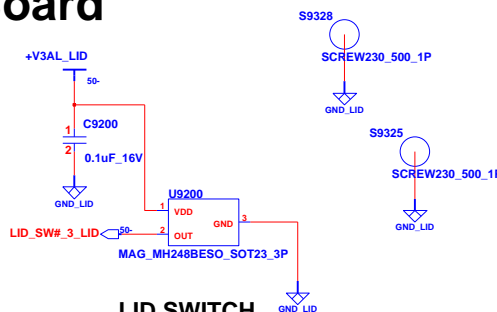
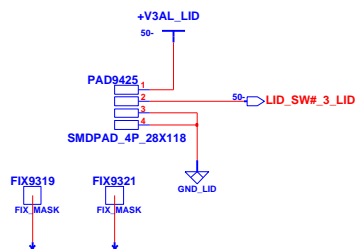
QUICK Launch LED



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LID SWITCH BOARD Board

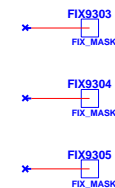
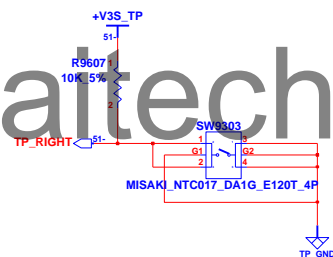
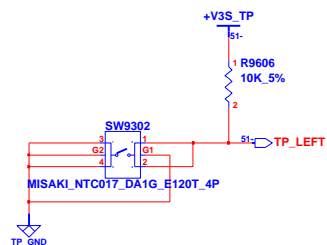
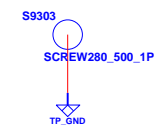
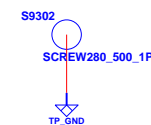
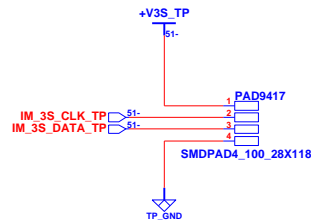
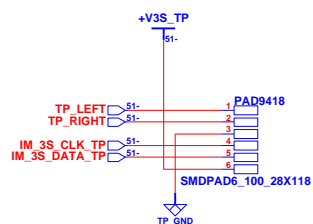
PCB_PN:6050A2414801



LID SWITCH
Reference Number 9200-9299

INVENTEC			
TITLE: PIXIES			
LID & Quick Launch & WLAN SW Board			
SIZE: A3	CODE: CS	DOC. NUMBER: 6050A24265-0-MTR	REV: X01
SHEET: 50		OF: 60	

CHANGE by: Drawer Name 30-Sep-2010



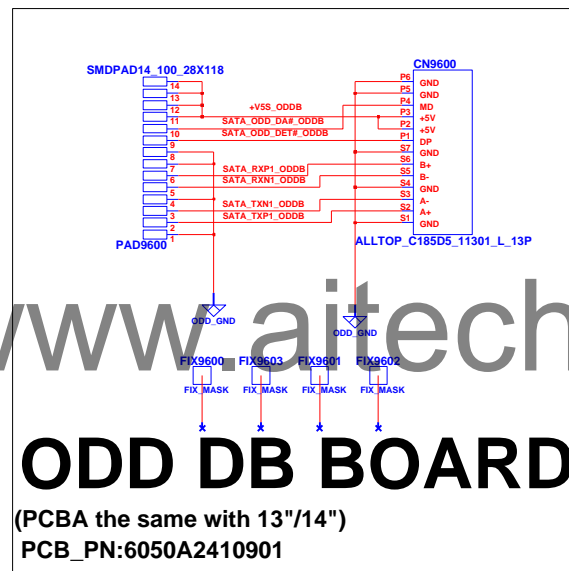
LEFT BUTTON

RIGHT BUTTON

PICK Button Board

PCB_PN:6050A2410601

INVENTEC			
TITLE PIXIES Pick Button Board			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
CHANGE by Drawer Name		30-Sep-2010	
SHEET		51 OF 60	



ODD DB BOARD

(PCBA the same with 13"/14")
PCB_PN:6050A2410901

INVENTEC			
TITLE PIXIES ODD DB Board			
SIZE A3	CODE CS	DOC. NUMBER 6050A24265-0-MTR	REV X01
CHANGE by Drawer Name		5-Oct-2010	
SHEET		52	OF 60

MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	Vendor	(Hz/MHz)
Ra	Rb	Rc	Rd		
0	0	0	0	Samsung(32MX32) - K4G10325FE-HC04	
0	0	0	1	Samsung(64MX32) - K4G20325FC-HC04	
0	0	1	0	Hynix(32MX32) - HSQ01H24A4FR-T2C	
0	0	1	1	Hynix(32MX32) - HSQ02H24MFR-T2C	
0	1	0	0	Elpidat(32MX32) - EDW1032ABG-50-F	
0	1	0	1	Elpidat(64MX32) - EDW2032ABG-50-F	
1	0	0	0	Samsung(32MX32) - K4G10325FE-HC04	
1	0	0	1	Samsung(64MX32) - K4G20325FC-HC04	
1	0	1	0	Hynix(32MX32) - HSQ01H24A4FR-T2C	
1	0	1	1	Hynix(64MX32) - HSQ02H24MFR-T2C	
1	1	0	0	Elpidat(32MX32) - EDW1032ABG-50-F	
1	1	0	1	Elpidat(64MX32) - EDW2032ABG-50-F	

